WARRANTY

Keithley Instruments, Inc. warrants this product to be free from defects in material and workmanship for a period of 1 year from date of shipment.

Keithley Instruments, Inc. warrants the following items for 90 days from the date of shipment: probes, cables, rechargeable batteries, diskettes, and documentation.

During the warranty period, we will, at our option, either repair or replace any product that proves to be defective.

To exercise this warranty, write or call your local Keithley representative, or contact Keithley headquarters in Cleveland, Ohio. You will be given prompt assistance and return instructions. Send the product, transportation prepaid, to the indicated service facility. Repairs will be made and the product returned, transportation prepaid. Repaired or replaced products are warranted for the balance of the original warranty period, or at least 90 days.

LIMITATION OF WARRANTY

This warranty does not apply to defects resulting from product modification without Keithley’s express written consent, or misuse of any product or part. This warranty also does not apply to fuses, software, non-rechargeable batteries, damage from battery leakage, or problems arising from normal wear or failure to follow instructions.

THIS WARRANTY IS IN LIEU OF ALL OTHER WARRANTIES, EXPRESSED OR IMPLIED, INCLUDING ANY IMPLIED WARRANTY OF MERCHANTABILITY OR FITNESS FOR A PARTICULAR USE. THE REMEDIES PROVIDED HEREIN ARE BUYER’S SOLE AND EXCLUSIVE REMEDIES.

NEITHER KEITHLEY INSTRUMENTS, INC. NOR ANY OF ITS EMPLOYEES SHALL BE LIABLE FOR ANY DIRECT, INDIRECT, SPECIAL, INCIDENTAL OR CONSEQUENTIAL DAMAGES ARISING OUT OF THE USE OF ITS INSTRUMENTS AND SOFTWARE EVEN IF KEITHLEY INSTRUMENTS, INC., HAS BEEN ADVISED IN ADVANCE OF THE POSSIBILITY OF SUCH DAMAGES. SUCH EXCLUDED DAMAGES SHALL INCLUDE, BUT ARE NOT LIMITED TO: COSTS OF REMOVAL AND INSTALLATION, LOSSES SUSTAINED AS THE RESULT OF INJURY TO ANY PERSON, OR DAMAGE TO PROPERTY.
Manual Print History

The print history shown below lists the printing dates of all Revisions and Addenda created for this manual. The Revision Level letter increases alphabetically as the manual undergoes subsequent updates. Addenda, which are released between Revisions, contain important change information that the user should incorporate immediately into the manual. Addenda are numbered sequentially. When a new Revision is created, all Addenda associated with the previous Revision of the manual are incorporated into the new Revision of the manual. Each new Revision includes a revised copy of this print history page.

<table>
<thead>
<tr>
<th>Revision/Addendum</th>
<th>Document Number 7065-901-01</th>
<th>Date</th>
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<tr>
<td>Revision A</td>
<td>7065-901-01</td>
<td>November 1986</td>
</tr>
<tr>
<td>Addendum A</td>
<td>7065-901-02</td>
<td>December 1986</td>
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<td>March 1987</td>
</tr>
<tr>
<td>Revision B</td>
<td>7065-901-01</td>
<td>March 1989</td>
</tr>
</tbody>
</table>

All Keithley product names are trademarks or registered trademarks of Keithley Instruments, Inc.
Other brand and product names are trademarks or registered trademarks of their respective holders.
The following safety precautions should be observed before using this product and any associated instrumentation. Although some instruments and accessories would normally be used with non-hazardous voltages, there are situations where hazardous conditions may be present.

This product is intended for use by qualified personnel who recognize shock hazards and are familiar with the safety precautions required to avoid possible injury. Read the operating information carefully before using the product.

The types of product users are:

**Responsible body** is the individual or group responsible for the use and maintenance of equipment, for ensuring that the equipment is operated within its specifications and operating limits, and for ensuring that operators are adequately trained.

**Operators** use the product for its intended function. They must be trained in electrical safety procedures and proper use of the instrument. They must be protected from electric shock and contact with hazardous live circuits.

**Maintenance personnel** perform routine procedures on the product to keep it operating, for example, setting the line voltage or replacing consumable materials. Maintenance procedures are described in the manual. The procedures explicitly state if the operator may perform them. Otherwise, they should be performed only by service personnel.

**Service personnel** are trained to work on live circuits, and perform safe installations and repairs of products. Only properly trained service personnel may perform installation and service procedures. Keithley products are designed for use with electrical signals that are rated Installation Category I and Installation Category II, as described in the International Electrotechnical Commission (IEC) Standard IEC 60664. Most measurement, control, and data I/O signals are Installation Category I and must not be directly connected to mains voltage or to voltage sources with high transient over-voltages. Installation Category II connections require protection for high transient over-voltages often associated with local AC mains connections. The user should assume all measurement, control, and data I/O connections are for connection to Category I sources unless otherwise marked or described in the Manual.

Exercise extreme caution when a shock hazard is present. Lethal voltage may be present on cable connector jacks or test fixtures. The American National Standards Institute (ANSI) states that a shock hazard exists when voltage levels greater than 30V RMS, 42.4V peak, or 60VDC are present. **A good safety practice is to expect that hazardous voltage is present in any unknown circuit before measuring.**

Users of this product must be protected from electric shock at all times. The responsible body must ensure that users are prevented access and/or insulated from every connection point. In some cases, connections must be exposed to potential human contact. Product users in these circumstances must be trained to protect themselves from the risk of electric shock. If the circuit is capable of operating at or above 1000 volts, **no conductive part of the circuit may be exposed.**

Do not connect switching cards directly to unlimited power circuits. They are intended to be used with impedance limited sources. **NEVER connect switching cards directly to AC mains.** When connecting sources to switching cards, install protective devices to limit fault current and voltage to the card.

Before operating an instrument, make sure the line cord is connected to a properly grounded power receptacle. Inspect the connecting cables, test leads, and jumpers for possible wear, cracks, or breaks before each use.

When installing equipment where access to the main power cord is restricted, such as rack mounting, a separate main input power disconnect device must be provided, in close proximity to the equipment and within easy reach of the operator.

For maximum safety, do not touch the product, test cables, or any other instruments while power is applied to the circuit under test. **ALWAYS remove power from the entire test system and discharge any capacitors before:** connecting or disconnecting cables or jumpers, installing or removing switching cards, or making internal changes, such as installing or removing jumpers.

Do not touch any object that could provide a current path to the common side of the circuit under test or power line (earth) ground. Always make measurements with dry hands while standing on a dry, insulated surface capable of withstanding the voltage being measured.
The instrument and accessories must be used in accordance with its specifications and operating instructions or the safety of the equipment may be impaired.

Do not exceed the maximum signal levels of the instruments and accessories, as defined in the specifications and operating information, and as shown on the instrument or test fixture panels, or switching card.

When fuses are used in a product, replace with same type and rating for continued protection against fire hazard.

Chassis connections must only be used as shield connections for measuring circuits, NOT as safety earth ground connections.

If you are using a test fixture, keep the lid closed while power is applied to the device under test. Safe operation requires the use of a lid interlock.

If a screw is present, connect it to safety earth ground using the wire recommended in the user documentation.

The symbol on an instrument indicates that the user should refer to the operating instructions located in the manual.

The symbol on an instrument shows that it can source or measure 1000 volts or more, including the combined effect of normal and common mode voltages. Use standard safety precautions to avoid personal contact with these voltages.

The **WARNING** heading in a manual explains dangers that might result in personal injury or death. Always read the associated information very carefully before performing the indicated procedure.

The **CAUTION** heading in a manual explains hazards that could damage the instrument. Such damage may invalidate the warranty.

Instrumentation and accessories shall not be connected to humans. Before performing any maintenance, disconnect the line cord and all test cables.

To maintain protection from electric shock and fire, replacement components in mains circuits, including the power transformer, test leads, and input jacks, must be purchased from Keithley Instruments. Standard fuses, with applicable national safety approvals, may be used if the rating and type are the same. Other components that are not safety related may be purchased from other suppliers as long as they are equivalent to the original component. (Note that selected parts should be purchased only through Keithley Instruments to maintain accuracy and functionality of the product.) If you are unsure about the applicability of a replacement component, call a Keithley Instruments office for information.

To clean an instrument, use a damp cloth or mild, water based cleaner. Clean the exterior of the instrument only. Do not apply cleaner directly to the instrument or allow liquids to enter or spill on the instrument. Products that consist of a circuit board with no case or chassis (e.g., data acquisition board for installation into a computer) should never require cleaning if handled according to instructions. If the board becomes contaminated and operation is affected, the board should be returned to the factory for proper cleaning/servicing.
7065 HALL EFFECT CARD SPECIFICATIONS
(1 year, 0°C-35°C; installed in scanner mainframe)

CONFIGURATION: Input characteristics and output matrix configurable for Van der Pauw or Hall Bar measurements. Input characteristics selectable for either low resistivity or high resistivity samples.

LOW RESISTIVITY MODE:

Input Voltage Operating Range: +8V to -8V.
Input Impedance: >10GΩ in parallel with less than 420pF.
Input Bias Current: <100pA.
Input Voltage Noise: <50nVp-p, 0.1 to 10Hz bandwidth.
Input to Output Resistance: <30Ω.

HIGH RESISTIVITY MODE:

Input Voltage Operating Range: +8V to -8V.
Input Impedance: >100kΩ in parallel with less than 3pF.
Input Bias Current: <150µA at 25°C. Doubles approximately every 10°C rise in ambient room temperature.
Input Voltage Noise: <10µVp-p, 0.1 to 10Hz bandwidth.
Output Resistance: <60Ω.

GENERAL:

ACCESSORIES SUPPLIED:
Model 4801 Low Noise Input Cable
Model 6167 Guarded Input Adapter
Model 7024-3 Triaxial Cable (3 ft.)
Model 7024-10 Triaxial Cable (10 ft.)
Model 4802-10 Low Noise Input Cable (10 ft.)
Model 7025-10 Triaxial Input Cable (10 ft.). Five supplied
Model 7008-6 IEEE-488 Cable (6 ft.)
Model 4851 BNC Shorting Plug
SC-72-0 Single Conductor Insulated Wire, Black (4 ft.). Two supplied
SC-72-9 Single Conductor Insulated Wire, White (4 ft.)
BG-5 Single Banana Plug, Black. Two supplied
BG-10-1 Single Banana Plug, White
BG-7 Double Banana Plug, Black
SC-8 2-Conductor Cable W/Shield (10 ft.)

MAXIMUM COMMON MODE VOLTAGE (analog ground to earth ground): 30V peak, dc to 60Hz sine wave.
ISOLATION: Analog ground to earth ground: Greater than 10Ω in parallel with 150pF.
WARMUP: 1 hour to rated specifications.
ENVIRONMENT: Operating: 0°C-35°C, up to 70% R.H.
           Storage: -25°C to 65°C.
DIMENSIONS, WEIGHT: 32mm high × 114mm wide × 272mm long (1 in. × 4 in. × 10 in.). Net weight: 434kg (15.5 oz.).

CONNECTORS:

CURRENT SOURCE INPUT: Two-lug female triaxial. Input high to low clamped at ±12V. Maximum Input: 100mA.
SAMPLE INPUTS: Four two-lug female triaxial. Outer shell is analog ground. Inner shield is driven guard. Maximum Input Overload (HI to analog ground or guard to analog ground): ±12V.
CURRENT MONITOR OUTPUT: Insulated female BNC.
MEASUREMENT OUTPUTS: Spring-loaded terminals. Accepts AWG. #18 to #24 wire. Maximum load: 1mA.

Specifications subject to change without notice.
RESISTIVITY MEASUREMENT ACCURACY:

Note: Resistivity accuracies based on sample excitation voltage which produces no more than 1mW power dissipation in sample; 5V maximum for high resistivity mode, 3V maximum for low resistivity mode.
HALL VOLTAGE LIMIT OF ERROR:

Notes:
1. Assumes 196 DMM on 300mV range for both modes; default resolution and filters.
2. For better performance below 500nV in the low resistivity mode, a Model 181 nanovoltmeter is recommended in place of the Model 196 DMM.
3. Assumes device under test at room temperature (18°-28°C).
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SECTION 1
General Information

Contains information on Model 7065 features, supplied accessories, as well as recommended equipment.

SECTION 2
Operation

Details installation of the Hall Card in a Model 705 or 706 Scanner, outlines connections to various types of specimens, and also discusses measurement considerations.

SECTION 3
Applications

Covers basic applications for Hall voltage and resistivity measurement of van der Pauw, bar, bridge, and parallelepiped samples, and includes example programs.

SECTION 4
Service Information

Contains performance verification, adjustment, and troubleshooting procedures for the Model 7065.

SECTION 5
Replaceable Parts

Lists replacement parts, and also includes component layout and schematic drawings for the Hall card.
SECTION 1
GENERAL INFORMATION

1.1 INTRODUCTION

This section contains general information about the Model 7065 Hall Card and is arranged as follows:

1.2 Features
1.3 Warranty Information
1.4 Manual Addenda
1.5 Safety Symbols and Terms
1.6 Specifications
1.7 Unpacking and Inspection
1.8 Repacking for Shipment
1.9 Recommended Equipment
1.10 Scanner Compatibility

1.2 FEATURES

The Model 7065 Hall Card is designed to assist in making resistivity and Hall voltage measurements on several types of semiconductor specimens. The unit is designed to be used in conjunction with a Keithley Model 705 or 706 Scanner along with a suitable current source and voltmeter. The Model 7065 incorporates the necessary on-card switching and buffering in order to minimize measurement complexity and maximize accuracy.

Key Model 7065 features include:

- Selectable input characteristics for either high- or low-resistivity measurements.
- Solid-state switching is used for measurement paths for higher reliability and lower thermal emfs.
- On-card, low-EMI power supply to maximize isolation and minimize noise effects.
- Provisions for guarded measurements to minimize the effects of leakage resistance and capacitance.
- On-card connection for a picoammeter to verify sample excitation current.

1.3 WARRANTY INFORMATION

Warranty information may be found on the inside front cover of this instruction manual. Should your Model 7065 require warranty service, contact the Keithley representative or authorized repair facility in your area for further information. When returning the card for repair, be sure to fill out and include the service form in order to provide the repair facility with the necessary information.

1.4 MANUAL ADDENDA

Any improvements or changes concerning the unit or manual will be explained in an addendum included with the unit. Please be sure to note these changes and incorporate them into the manual before operating or servicing the unit.

1.5 SAFETY SYMBOLS AND TERMS

The following symbols and terms may be found on the instrument or used in this manual.

The △ symbol on the instrument indicates that the user should refer to the operating instructions.

The ⚡ symbol on an instrument shows that high voltage may be present on the terminal(s). Use standard safety practices to avoid personal contact with these voltages.

The WARNING heading used in this manual explains dangers that might result in personal injury or death. Always read the associated information very carefully before performing the indicated procedure.

The CAUTION heading used in this manual explains hazards that could damage the unit. Such damage may void the warranty.
1.6 SPECIFICATIONS

Model 7065 specifications are located at the front of this manual. These specifications are exclusive of the scanner mainframe specifications, which may be found in the instruction manuals for those instruments.

1.7 UNPACKING AND INSPECTION

Upon receiving the Model 7065, carefully unpack the unit from its shipping carton and inspect the card for any obvious signs of physical damage. Report any such damage to the shipping agent immediately. Save the original packing carton for possible future reshipment.

The following items are included with every Model 7065 order:

- Model 7065 Hall Effect Card.
- Standard accessories (Table 1-1)
- Additional accessories as ordered.

If an additional instruction manual is required, order the manual package (Keithley Part Number 7065-901-00). The manual package includes an instruction manual and any applicable addenda.

1.8 REPACKING FOR SHIPMENT

Should it become necessary to return the Hall Card for repair, carefully pack the unit in the original packing carton or its equivalent. Only the Model 7065 itself need be returned.

Be sure to include the following information:

- Advise as to the warranty status.
- Write ATTNENTION REPAIR DEPARTMENT on the shipping label.
- Fill out and include the service form at the back of this manual.

1.9 RECOMMENDED EQUIPMENT

Table 1-2 summarizes the instrumentation necessary for a Hall system. This list does not include necessary usersupplied equipment such as a cryostat, electromagnet, magnet power supply, or IEEE-488 controller.

The Keithley equipment summarized in Table 1-2 is briefly described on the next page.

Table 1-1. Supplied Accessories

<table>
<thead>
<tr>
<th>Qty</th>
<th>Keithley Model</th>
<th>Description</th>
<th>Application</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>4801</td>
<td>Low noise coaxial cable</td>
<td>Connects 485 input to 7065 current monitor output</td>
</tr>
<tr>
<td>1</td>
<td>4802-10</td>
<td>Low noise coaxial cable (10')</td>
<td>Connects 485 directly to sample for bar, 6 and 8 contact samples</td>
</tr>
<tr>
<td>5</td>
<td>7025-10</td>
<td>Triaxial cable (10')</td>
<td>Four to connect sample to 7065 sample inputs, one to connect 220 directly to sample in 6 and 8 contact samples</td>
</tr>
<tr>
<td>1</td>
<td>6167</td>
<td>Guarded input adapter</td>
<td>Allows 220 to drive current source input cable at guard potential.</td>
</tr>
<tr>
<td>1</td>
<td>7024-3</td>
<td>3' triax cable</td>
<td>Attaches 220/6167 output to 7065 current source input.</td>
</tr>
<tr>
<td>1</td>
<td>7024-10</td>
<td>10' triax cable</td>
<td>Same as above</td>
</tr>
<tr>
<td>1</td>
<td>4851</td>
<td>BNC short</td>
<td>Shorts 7065 current monitor output.</td>
</tr>
<tr>
<td>1</td>
<td>7008-6</td>
<td>6' IEEE cable</td>
<td>Connects bus controller to the above instruments</td>
</tr>
<tr>
<td>2</td>
<td>SC-72-0</td>
<td>Black wire (4')</td>
<td>Connect analog ground.</td>
</tr>
<tr>
<td>1</td>
<td>SC-72-9</td>
<td>White (4')</td>
<td>Connect 220 guard to 7065 GD terminal.</td>
</tr>
<tr>
<td>2</td>
<td>BG-5</td>
<td>Banana Plug (black)</td>
<td>Terminates black connecting wires.</td>
</tr>
<tr>
<td>1</td>
<td>BG-10-1</td>
<td>Banana plug (white)</td>
<td>Terminates white connecting wire.</td>
</tr>
<tr>
<td>1</td>
<td>BG-7</td>
<td>Black double banana plug</td>
<td>Used with SC-4 to connect DMM to card.</td>
</tr>
<tr>
<td>1</td>
<td>SC-8</td>
<td>2-conductor cable with shield</td>
<td>Used with BG-7 to connect DMM to card.</td>
</tr>
</tbody>
</table>
Table 1-2. Recommended Hall System Equipment

<table>
<thead>
<tr>
<th>Keithley Model Number</th>
<th>Description</th>
<th>Use</th>
</tr>
</thead>
<tbody>
<tr>
<td>196</td>
<td>6½ Digit DMM</td>
<td>Measure sample voltage</td>
</tr>
<tr>
<td>220</td>
<td>Current Source</td>
<td>Supply sample current</td>
</tr>
<tr>
<td>485*</td>
<td>Picoammeter Scanner</td>
<td>Monitor sample current</td>
</tr>
<tr>
<td>705 or 706</td>
<td></td>
<td>Control Model 7065 Hall Effect Card</td>
</tr>
</tbody>
</table>

*Not essential but recommended for best accuracy.

NOTE: This listing does not include required user-supplied equipment such as cryostat, electromagnet and power supply, or IEEE-488 bus controller.

Model 196 DMM: The Model 196 is recommended for measuring the voltage across the sample under test. Important Model 196 features include:

- 100nV resolution.
- High input resistance (>1GΩ on 300mV and 3V ranges) for minimum sample loading.
- 6½ digit display resolution.
- Autoranging
- IEEE-488 bus operation.

Model 220 Current Source: The Model 220 Current Source is recommended for applying the current to the sample under test. Key Model 220 features include:

- ±0.5pA to ±101mA DC current output.
- 10¹⁶ Ω output resistance.
- Programmable voltage compliance limit.
- Complete IEEE-488 bus programmability.

Model 485 Picoammeter: The Model 485 Picoammeter can be used to monitor the current being applied to the sample under test. Although not absolutely essential for a measurement system, the use of the Model 485 is recommended to maximize accuracy, especially when testing high-resistivity samples. Important Model 485 features include:

- 0.1pA resolution, 2mA maximum input current.
- 200µV maximum voltage burden
- Autoranging.

- 4½ digit display resolution.
- IEEE-488 bus operation (with option 4853).

Models 705 and 706 Scanners: The Model 7065 Hall Effect Card is designed to plug into the Models 705 or 706 Scanners. The Model 705 can accommodate two cards, while the Model 706 can handle up to 10 scanner cards. Other key features common to both scanner mainframes include:

- Full IEEE-488 bus programmability.
- Daisy chain operation allowing up to four slave mainframes to be controlled with one master.
- Front panel programs for easier system configuration.
- Built-in day/time clock for time stamping of data.

1.10 SCANNER COMPATIBILITY

The Model 7065 is designed to be used with the Keithley Models 705 and 706 Scanners. Note, however, that there are certain limitations when using the card in the Model 705.

Limitations for the Model 705 involve the software revision level, which must be B5 or later (the software revision level is displayed as part of the power-up cycle). The reason for this limitation is that Rev. B4 and earlier software does not support the matrix mode, which must be used with the Model 7065. Earlier versions can be upgraded; contact your Keithley representative or the factory for details.
SECTION 2
OPERATION

2.1 INTRODUCTION

This section contains information on card installation, connections, test configurations, and scanner programming, and is arranged as follows:

2.2 Handling Precautions: Covers precautions that should be observed when handling the Hall effect card.

2.3 Model 7065 Installation: Details installation in a Model 705 or 706 scanner mainframe.

2.4 Connections: Covers the various connectors on the card and how to connect it to other instruments.

2.5 Matrix and Test Configurations: Discusses 7065 matrix and typical system configurations.

2.6 Guarding Methods: Describes methods of source input guarding, and discusses sample input guarding as well as guarding principles.

2.7 Low and High Resistivity Selection: Covers Model 7065 setup requirements for low and high resistivity samples, and summarizes input characteristics and tradeoffs for each.

2.8 Front Panel Scanner Programming: Summarizes programming steps necessary to control the Model 7065 from the front panel of the scanner mainframe.

2.9 IEEE-488 Bus Scanner Programming: Reviews method to control the Model 7065 through a scanner mainframe over the IEEE-488 bus.

2.10 Measurement Considerations: Covers important factors that must be taken into account when making measurements with the Model 7065.

2.2 HANDLING PRECAUTIONS

Because the Model 7065 is designed for very high impedance measurements, care must be taken when handling the card to avoid contamination from foreign materials such as body oils. Such contamination can drastically lower channel isolation and leakage resistances, degrading performance.

To avoid possible contamination, always grasp the card at the edges. Although the card shields protect most of the circuit board, it is a good idea to avoid touching the exposed board areas. If you remove the shields for any reason, be sure not to touch the exposed board or any components underneath. Particularly sensitive areas include the sample inputs from the sample input jacks to the inputs of the buffer amplifiers.

Dirt build-up over a period of time is another possible contaminating factor. To avoid potential problems, operate the scanner only in a clean environment. If contamination is suspected, the card should be carefully cleaned using the procedure outlined in paragraph 4.2 of this manual.

2.3 MODEL 7065 INSTALLATION

The following paragraphs discuss Model 7065 Hall Effect Card installation in both the Models 705 and 706 Scanners. Note that some connections must be made to the Model 7065 before installation, as discussed in paragraph 2.4.

WARNING
Turn off the scanner power and disconnect the line cord before installing or removing scanner cards.

CAUTION
Leave the Model 7065 in its anti-static bag until ready for installation to avoid possible static damage.
2.3.1 Installation in the Model 705 Scanner

Refer to Figure 2-1 and install or remove the Model 7065 Hall Card as follows:

1. To install the card, slide it into the desired rear panel slot with the component (large shield) side facing up. Make sure the card edges are properly aligned with the grooves in the receptacle.
2. Once the card is fully seated, lock the card by placing the tabs into the locked position, as shown in Figure 2-1.
3. To remove the card, first unlock the tabs by pulling them outward, then grasp the end of the card at the edges and pull the card free of the scanner.

2.3.2 Installation in the Model 706 Scanner

Install the Model 7065 in the Model 706 Scanner using Figure 2-2 as a guide.

1. To install the Hall card, slide it into the desired vertical slot. Looking from the rear, slots are numbered from 1 to 10, left to right. Note that the component (large shield) side of the card should face to the left of the slot. Make certain the card edges are properly aligned with the top and bottom grooves in the slot.
2. Once the card is aligned with the grooves, push the card as far as it will go into the slot until it is properly seated into the slot connector. After the card is properly seated, lock the card with the tabs, as shown in Figure 2-2.
3. To remove the card, unlock the tabs by pulling them outward as shown in Figure 2-2. Grasp the end of the card and pull it free of the mainframe.

Figure 2-1. Model 705, Card Installation
Figure 2-2. Model 706, Card Installation
2.4 CONNECTIONS

The following paragraphs discuss the card connector and their purposes and also show typical connecting schemes.

2.4.1 Card Connectors

**WARNING**
Maintain inputs and outputs within 30V of earth ground. Failure to observe this precaution may result in a shock hazard.

The layout of the Model 7065 is shown in Figure 2-3. Connectors on the card are:

CURRENT SOURCE INPUT: A two-lug triaxial connector intended for applying the test current. Note that the inner shield can be connected to source guard using one of the methods described in paragraph 2.6. The Keithley Model 220 is the recommended current source. Maximum allowable input current is 100mA.

SAMPLE INPUTS (1 through 4): These are two-lug triaxial connectors, with the inner ring driven at guard potential, while the shell is connected to analog ground. Maximum input overload is 12V.

CURRENT MONITOR OUTPUT: An insulated BNC connector recommended for use with a Keithley Model 485 Picoammeter. Maximum input overload is 10mA when using the Model 485.

**CAUTION**
Be careful not to interchange triax and BNC connectors to avoid damaging them.

**NOTE**
The caps should be kept in place on the connectors when not in use to prevent possible contamination, which could degrade performance.

Terminal Block (J2): These thumb-operated, spring-loaded connectors accept 18-24 gauge wire. Three of the terminals are intended for voltmeter connections, while the remainder allow connection to analog ground or external guard. Terminal assignments are summarized in Table 2-1. Maximum load current is 1mA.

### Table 2-1. Terminal Strip Connections

<table>
<thead>
<tr>
<th>Terminal</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Analog ground</td>
</tr>
<tr>
<td>2</td>
<td>Analog ground</td>
</tr>
<tr>
<td>3</td>
<td>Hall Bar LO output</td>
</tr>
<tr>
<td>4</td>
<td>LO output</td>
</tr>
<tr>
<td>5</td>
<td>HI output</td>
</tr>
<tr>
<td>6</td>
<td>Guard input</td>
</tr>
</tbody>
</table>
2.4.2 Recommended Cables and Wires

Table 2-2 summarizes cables recommended for test connections. These cables are supplied with the card as standard equipment.

The Model 7024 cables are intended for connecting the Model 220 current source to the Model 7065. The Model 7025 unterminated triaxial cables are intended for sample connections. The coaxial cables are to be used for current monitor output connections. The Model 4801 connects the card to the picoammeter, while the Model 4802 allows the picoammeter to be connected directly to the sample.

Connecting wires between the terminal block and external equipment are supplied. The shielded cable/dual banana plug should be used to connect the DMM to the card, while the white and black wires can be used for guard and analog ground connections, respectively.

Table 2-2. Recommended Cables

<table>
<thead>
<tr>
<th>Cables</th>
<th>Used with</th>
</tr>
</thead>
<tbody>
<tr>
<td>Model 7025-10 triaxial cables (unterminated)</td>
<td>Sample inputs</td>
</tr>
<tr>
<td>Model 4801 BNC-to-BNC coaxial cable</td>
<td>Current inputs</td>
</tr>
<tr>
<td>Model 4802-10 BNC Cable (unterminated)</td>
<td>Current monitor output</td>
</tr>
<tr>
<td>Model 7024 triaxial cables SC-72 connecting wires</td>
<td>Current monitor output</td>
</tr>
<tr>
<td>2 - black</td>
<td>Current source input</td>
</tr>
<tr>
<td>1 - white</td>
<td>Analog ground</td>
</tr>
<tr>
<td>SC-8/BG-7</td>
<td>Guard</td>
</tr>
<tr>
<td></td>
<td>Red to DMM HI (term 5)</td>
</tr>
<tr>
<td></td>
<td>black/shield to DMM LO (term 4) on J2.</td>
</tr>
</tbody>
</table>

NOTE: These cables are supplied.

2.4.3 Cable and Wire Preparation

The necessary cables, wires, and connectors are supplied with the Model 7065; however, these cables and wires must be prepared before use as described below.

Single Wire/Banana Plug Preparation

As summarized in Table 2-2, color-coded wires are provided for making various connections between the Model 7065 and other instruments. A matching color-coded banana plug is included to mate with appropriate jacks on the test instruments.

NOTE

A banana plug should always be used to make the connection to a banana jack on an instrument to ensure proper contact.

The supplied SC-72 wires should be prepared and connected to the supplied banana plugs as follows. Refer to Figure 2-4 for the various steps.

1. Strip the end of the wire to be connected to the banana plug approximately ½ inch, then twist the strands together.
2. Unscrew the body of the matching color single banana plug and insert the stripped end of the wire through the small hole in the metal part of the plug so the wire comes through the small hole as shown in Figure 2-4.
3. Wrap the wire around the plug base in the same direction as the plastic body screws on, then screw on the plastic body securely.
4. Strip the opposite end of the wire approximately ⅛ inch and twist the strands together. This end is to be connected to the terminal block of the Model 7065.

![Figure 2-4. Single Wire Preparation](image-url)
Dual Banana Plug Preparation

A dual banana plug (BG-7) and length of shielded, two-conductor cable (SC-8) is provided with the Hall card. These items are to be used to connect the DMM to the Model 7065.

NOTE
To minimize noise, the dual banana plug/shield wire combination should always be used to connect the Model 7065 to the DMM instead of separate wires.

The dual banana plug and 2-conductor shielded cable can be prepared as follows (see Figure 2-5).

1. Strip back about 1¾ inch of outer insulation from the SC-8 shielded 2-conductor wire.
2. Unravel the shield braid and twist the strands together.
3. Strip approximately ¼ inch of insulation from both the red and black wires.
4. Twist the shield braid and black wires together tightly. Also twist the strands on the red wire together.
5. Slip the wire through the back hole in the dual banana plug, then place the red wire end in the hole in the banana plug without the ridge on the body.
6. Slip the black/shield wire end through the hole in the banana plug closest to the body ridge.
7. Secure both red and black wires by tightening the screws for each banana plug (these screws are accessible inside the body directly behind each banana plug). Be careful not to tighten them too tightly.
8. Strip approximately 1 inch of the insulation off the opposite end of the cable, then unravel and shield and twist the strands together.
9. Strip the red and black wire insulation approximately 3/8 inch.
10. Twist the black wire and shield together. Also twist the red wire strands together.
11. The cable is now ready for connection to the terminal block.

Figure 2-5. Shielded Cable Preparation
Triaxial Cable Preparation

The one end of each Model 7025 triaxial cable must be prepared as follows before it can be connected to the sample under test. Refer to Figure 2-6.

1. Using an X-Acto knife, cut and strip back the outer insulation about 1½ inches.
2. Remove the piece of insulation, then cut away the outer shield back as far as the insulation is stripped.
3. Carefully strip insulation on the inner shield one inch, then cut the shield off.
4. Carefully strip the insulation on the inner conductor to the desired length, then twist the strands together.
5. Repeat steps 1 through 4 for the remaining triaxial cables.

NOTE
Make sure the inner and outer shields do not touch one another.

Coaxial Cable Preparation

In some instances (bar and bridge samples), the picoammeter must be connected directly to the sample using a Model 4802 coaxial cable. Before use, the coaxial cable should be prepared as follows:

1. Cut back the outer insulation about one inch, then remove it.
2. Remove the exposed shield by cutting it back as far as the insulation.
3. Strip the inner insulation to the desired length, then twist the strands together.

2.4.4 Van der Pauw Sample Connections

Figure 2-7 shows typical connections for measurement made using the van der Pauw method, and Figure 2-8 gives an equivalent circuit.

Make connections as follows:

1. Connect the Model 6167 guard adapter to the Model 220 triax output. Connect the Model 6167 guard output to the Model 220 GUARD jack.
2. Connect the Model 6167 Input to the CURRENT SOURCE INPUT jack using a 7024-3 or 7024-10 triaxial cable.

NOTE
This setup assumes that the guarded source input configuration is to be used. See paragraph 2.6 for more information on guarding.

3. Connect the four terminals of the sample under test to the SAMPLE INPUTS using supplied triaxial cables. See Figure 2-6 for cable stripping instructions.
4. Connect the Model 485 Picoammeter to the CURRENT MONITOR OUTPUT jack on the Mode 7065 using the supplied Model 4801 low-noise cable (use the 4851 shorting plug if no picoammeter is to be used).
5. Connect Model 196 DMM LO to terminal 4 of the Measurement Output terminal strip, and connect DMM HI terminal to terminal 5 of the terminal strip. Use the dual banana plug/cable for DMM connections.
Figure 2-7. Connections for Van der Pauw Samples
Figure 2-8. Equivalent Circuit for Van der Pauw Connections
2.4.5 Bar Type Sample Connections

Hall Voltage Measurements

Use the basic scheme shown in Figure 2-9 for Hall voltage measurements on bar type samples. Figure 2-10 shows an equivalent circuit.

Make connections as follows:

1. Connect the sample to the SAMPLE INPUTS, as shown in Figure 2-9. Use the supplied 7025 triaxial cables (see paragraph 2.4.3 for preparation).
2. Mount the Model 6167 guarding adapter on the Model 220 and connect the guard lead to the Model 220 GUARD terminal. Connect a Model 7024 triax cable between the Model 6167 input jack and the Model 7065 Current Source Input.

NOTE

This connecting method assumes guarded source input operation. See paragraph 2.6 for more information on guarding.

3. Connect the picoammeter HI input terminal directly to the sample under test through a Model 4802 coaxial cable. Connect a black wire/banana plug between analog ground on the terminal block and Model 485 analog output low.
4. Connect a suitable potentiometer between terminals 4 and 5 of the terminal strip. See paragraph 3.6.2 for details on selecting the correct potentiometer value.
5. Connect VOLTS/OHMS LO to terminal 3 of the strip. Connect DMM HI to the wiper of the potentiometer using the dual banana plug/shield wire.

Resistivity Connections

Connections for determining the resistivity of bar type samples are essentially the same as for Hall voltage except for the way the DMM is connected. As shown in Figure 2-11, DMM VOLTS/OHMS LO should be connected to terminal 4 of the strip, and DMM HI should be connected to terminal 5. Figure 2-12 shows an equivalent circuit of the test configuration.
Figure 2-9. Connections for Hall Voltage Measurements of Bar Type Samples
Figure 2-10. Equivalent Circuit for Hall Voltage Measurements of Bar Type Samples
Figure 2-11. Connections for Resistivity Measurements of Bar Type Samples
Figure 2-12. Equivalent Circuit for Resistivity Measurements of Bar Type Samples
2.4.6 Bridge and Parallelpiped Type Sample Connections

Connections for 6-contact samples are shown in Figure 2-13, while Figure 2-14 gives an equivalent circuit. Figures 2-15 and 2-16 summarize the connections for 8-contact samples.

Make the required connections as follows:

1. Connect the required sample terminals to the four SAMPLE inputs using supplied Model 7025 triaxial cables.
2. Using the supplied 4802 coaxial cable, connect the HI terminal of the picoammeter to the indicated sample terminal.
3. Connect a Model 7025 triaxial cable to the Model 220, as indicated. Connect current source HI (center conductor) to the sample, as shown on the appropriate diagram.
4. Current source low and picoammeter low must be connected together. The method of connection depends on whether or not current input guarding is used, as follows:
   A. Unguarded: Connect picoammeter analog output LO to the output common terminal of the Model 220 Current Source. The ground link should be removed.
   B. Guarded: Connect picoammeter analog output LO to the output common terminal of the Model 220 current source. The inner shield should be left floating at the sample end. The Model 6167 must be used to guard the cable, as discussed in paragraph 2.6.
5. Regardless of the current input guarding configuration, Model 220 GUARD must be connected to the GD terminal (6) on the Model 7065 in order to drive the protection circuits on the card.
6. Connect the DMM to the Hall Card as indicated on the appropriate diagram. Use the shielded cable/dual banana plug to make connections.
Figure 2-13. Connections for 6-Contact Samples
Figure 2-14. 6-Contact Equivalent Circuit (Current 1-2, Voltage 6-4)
Figure 2-15. Connections for 8-Contact Samples
Figure 2-16. 8-Contact Sample Equivalent Circuit (Current 1-2, Voltage 6-4 shown)

NOTE:
HIGH RESISTIVITY MODE SHOWN
BUFFERS ARE BYPASSED
IN LOW RESISTIVITY MODE
2.4.7 Shorting the Current Monitor Output

Normally, a system will be setup with a Model 485 or similar picoammeter to accurately measure the sample excitation current. As an alternative, you can configure your system for use without a picoammeter by placing the Model 4851 BNC shorting plug on the CURRENT MONITOR OUTPUT jack in place of the picoammeter connecting cable. This shorting plug is necessary to complete the circuit for the current path. Note that shorting the CURRENT MONITOR OUTPUT is not necessary for bar, 6-contact or 8-contact samples because the picoammeter is connected directly to the sample for those measurements. (In cases where a picoammeter is not used, short out the picoammeter on the wiring diagram).

Another situation requiring use of the shorting plug is when the sample excitation current exceeds the 2mA measurement capability of the Model 485 Picoammeter. In this case, the Model 485 should be disconnected from the Model 7065, and the Model 4851 shorting plug connected in its place.

2.4.8 IEEE-488 Bus Connections

Connect each instrument to the IEEE-488 bus using the supplied Model 7008 IEEE-488 cables. Refer to the instrument instruction manuals for pertinent details.

2.5 MATRIX AND TEST CONFIGURATIONS

The following paragraphs discuss the matrix configuration of the Model 7065 and detail a basic Hall measurement system.

2.5.1 Model 7065 Matrix

The Model 7065 is organized into a 5 X 4 (5 column by 4 row) matrix, as shown in Figure 2-17. Each intersection of a column and row is called a crosspoint and is designated by a small circle on the diagram. By appropriate programming, a particular row can be connected to the desired column by closing the appropriate crosspoint. The convention used in this manual always designates a crosspoint in column, row format. For example, contact 3,4 refers to column 3, row 4 of the matrix.

Source and measurement devices are connected to the columns, while the samples are connected to the rows. For example, the picoammeter and current source are connected to columns 1 and 2 respectively, while the voltmeter is connected to columns 3 and 4. Column 5 is reserved for Hall bar measurements except for 5,4, which selects low/high resistivity operation.

The four sample inputs are applied to the X1 buffer amplifiers, which have a very high input resistance to minimize loading. In the low-resistivity mode, however, these buffers are effectively bypassed by FET switches. Although not shown on the diagram, the buffers also provide a driven guard for the four sample inputs to minimize the effects of leakage resistance and capacitance.

To demonstrate the versatility of the switching matrix, let us assume that we wish to apply a current through terminals 1 and 2 of the sample and measure the voltage between terminals 4 and 3 of that sample. To apply the current, we would close 2,1 and 1,2, while to measure the voltage, contacts 3,4 and 4,3 would be closed. In this case, current would flow from the source, through 2,1 into terminal 1 of the sample. From there, current would flow out terminal 2, through 1,2, into the picoammeter back to analog ground to complete the path. The act of closing 3,4 and 4,3 would connect the voltmeter to measure the voltage between terminals 4 and 3 through the buffer amplifiers (high resistivity mode).

More information on scanner programming may be found in paragraphs 2.8 and 2.9, while Section 3 covers measurement methods in more detail.

2.5.2 Test System

The basic configuration of an IEEE-488 based Hall measurement system is shown in Figure 2-18. A system used solely for resistivity measurements would not require the electromagnet or magnet power supply.
Figure 2-17. Model 7065 Matrix Configuration
Figure 2-18. Basic Hall System Configuration
Equipment in the system includes:

220 Current Source: Applies the current to the sample under test.
485 Picoammeter: Measures the current through the sample under test.
7065 Hall Card: Switches and buffers applied currents and voltages.
705 or 706 Scanner: Controls and supplies power to the 7065 Hall Card.
196 Voltmeter: Measures the voltage across the sample under test.
IEEE-488 Controller: Provides the intelligence to control the instruments in the system.
Electromagnet: Provides an accurately known magnetic flux for the sample under test (A Hall sensor is sometimes used to measure the magnetic field as well).
Magnet Power Supply: Supplies the necessary current for the electromagnet.
Cryostat: Keeps the sample at the desired test temperature.
Temperature Control: Controls the cryostat to maintain the desired temperature.

2.6 GUARDING METHODS

The following paragraphs discuss principles of guarding, methods of guarding on the current source input, and guarding of the sample inputs.

2.6.1 Principles of Guarding

Guarding consists of using a conductor driven by a low-impedance source to totally surround the leads carrying a high-impedance signal. If the output voltage of this low-impedance source is kept at the same potential as the signal itself, the result will be reduced leakage effects, decreased response time, and lower noise.

To approach the concept of guarding, let us first consider the unguarded circuit shown in Figure 2-19. Here, the sample voltage is represented by $E_S$, while the equivalent resistance is $R_S$. The cable leakage impedance is $Z_L$, and $E_M$ represents the actual measured voltage.

The sample resistance and cable impedance form a voltage divider that attenuates the sample voltage as follows:

$$E_M = \frac{Z_L E_S}{Z_L + R_S}$$

Thus, to keep the error caused by leakage resistance under 0.1%, the leakage resistance must be at least 1000 times the sample resistance. For low to medium resistivity samples, the leakage resistance is generally sufficiently high so as to have minimal effects. However, with very high resistivities, the errors due to leakage resistance in unguarded circuits can be intolerably high.
Figure 2-19. Unguarded Circuit
Guarding the circuit minimizes these effects by driving the cable shield at signal potential, as shown in Figure 2-20. Here, the Model 7065 buffer amplifier, which has high input impedance and low output impedance, is used to drive the inner shield. Since the amplifier has unity gain, the potential across the leakage resistance is essentially zero, so that virtually no current flows. Leakage between the inner shield and ground (outer shield) may be considerable, but it is of little consequence since that current is supplied by the buffer amplifier rather than by the sample signal voltage itself.

As an example, consider the situation where a 10GΩ resistance is being measured through a cable that has 200pF of distributed capacitance. This combination results in an RC time constant of 2sec. Thus, at least 10 seconds must be allowed to allow the circuit to settle to within 1% of final value. In contrast, guarding the circuit could result in a more than hundred-fold reduction in settling time.

### 2.6.2 Current Source Input Guarding

When testing high resistivity samples, the current source input must also be guarded by using the driven guard from the current source itself. The following paragraphs discuss guarded and unguarded operation of the current source input and various connecting methods.

**NOTE**

Although you do have the option of selecting guarded or unguarded operation, there is no harm in using the guarded mode for all measurements; it works for all resistivities.

#### Guarding Jumpers

As shown in Figure 2-21, two jumpers set up the current source input for either unguarded or guarded operation. The jumper configurations shown at the top of the diagram indicate which of two configurations apply as follows:

1. Unguarded (A): This method can be used only for relatively low resistivity (10^Ω or less resistance) samples (low-resistivity mode).
2. Guarded (B): This configuration is required for use with the high-resistivity mode. Note that the Model 6167 Guarded Adapter must be used in conjunction with the Model 220 in order to drive the inner shield of the triaxial cable at guard potential.

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2-26
Figure 2-21. Guarding Jumper Configurations

NOTE: SEE TEXT FOR GUARDING RECOMMENDATIONS

A. UNGUARDED

B. GUARDED

GUARDING JUMPERS UNDER SHIELD

SHIELD
OPERATION

Changing Jumper Settings

From the factory, the Model 7065 is setup for the guarded configuration. Use the following procedure to change or verify the positions of these jumpers.

1. Turn off the scanner mainframe power and unplug the power line cord. Remove the card from the scanner, if installed.
2. Remove the large analog shield from the Model 7065 card, as shown in Figure 2-22.

**CAUTION**

Do not touch the board surface or any components on the board as doing so may degrade performance. Handle the board only by the edges.

3. Place the two jumpers in the desired position(s), as shown in Figure 2-21. Use a pair of needle-nose pliers to avoid touching the PC board.
4. Replace the shield and make additional connections (if necessary) as outlined below before installing the card.

Guarded Connections

The current source input should be guarded when the card is in the high resistivity mode (paragraph 2.7). Figure 2-24(a) shows the basic connections, which are outlined as follows.

1. Attach the supplied Model 6167 Guard Adapter to the OUTPUT jack of the Model 220.
2. Connect the Model 6167 adapter wire to the Model 220 GUARD jack.
3. Place the adapter switch in the guarded position.
4. Connect the Model 7024 triaxial cable between the triax jack on the adapter and the Model 7065 Current Source Input.

An equivalent circuit of this configuration is shown in Figure 2-24(b). Current source output HI is connected to the Hall card input HI through the center cable conductor. Guard connection is made through the Model 6167 to the inner shield of the cable, while output LO now appears on the outer cable shield, and is connected to analog ground in the scanner card.

Unguarded Connections

Use the basic procedure below to connect the Model 220 to the Model 7065 using Figure 2-23 as a guide. Note that this configuration should be used only with the card in the low-resistivity mode, as discussed in paragraph 2.7.

1. Connect the Model 7024 triaxial cable between the Model 220 OUTPUT jack and the Model 7065 CURRENT SOURCE INPUT.
2. Connect a supplied insulated white (with white banana plug) wire between the Model 220 GUARD terminal and terminal 6 (GD) of the terminal block on the Model 7065.

**CAUTION**

Model 220 GUARD must be connected to Model 7065 guard even though the cable itself is not guarded. The Model 220 guard is used to drive the protection circuits in the Hall card. These circuits protect the source input from damage caused by excessive voltages.

2.6.3 Sample Input Guarding

In order to minimize the effects of leakage resistance and capacitance, the shield of each input cable is guarded by driving the inner shield with the output of the respective buffer amplifier, as shown in Figure 2-25. (Note that the outer shield is at analog ground potential and is not guarded.) The same guard potential also surrounds the contacts of the associated relay in order to minimize leakage effects of the switching circuits. Note that guarding is used for both high and low resistivity configurations even though the buffers are effectively switched out of the circuit when the card is configured for low resistivity. Paragraph 2.7 covers resistivity mode selection in detail.
Figure 2-22. Shield Removal
Figure 2-23. Connections for Unguarded Current Source Input
Figure 2-24. Connections for Guarding Using 6167 Adapter
Figure 2-25. Equivalent Circuit for Input Guarding
2.7 RESISTIVITY SELECTION

The Model 7065 may be operated in either the low or high resistivity mode, as discussed in the following paragraphs.

2.7.1 Selecting the Resistivity Setup

The SAMPLE INPUTS can be programmed for low or high resistivity by controlling the state of crosspoint 5,4 (column 5, row 4). To select high resistivity, close crosspoint 5,4; to select low resistivity, open crosspoint 5,4. Table 2-3 summarizes these programming states. Scanner programming is discussed in paragraphs 2.8 and 2.9.

Note that all four sample inputs are controlled simultaneously by selecting crosspoint 5,4.

Table 2-3. Resistivity Select Summary

<table>
<thead>
<tr>
<th>Column 5, Row 4 State</th>
<th>Resistivity Setup</th>
</tr>
</thead>
<tbody>
<tr>
<td>Open</td>
<td>Low resistivity</td>
</tr>
<tr>
<td>Closed</td>
<td>High resistivity</td>
</tr>
</tbody>
</table>

2.7.2 Input Characteristics

Table 2-4 summarizes input characteristics for the low and high resistivity setups. In addition to affecting the resistivity setup, selection also affects input current, noise, and offset voltages. Note that the input characteristics of the low resistivity setup are exclusive of the voltmeter, which can also affect the sample under test.

Table 2-4. Input Characteristic Summary

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Low Resistivity Setup</th>
<th>High Resistivity Setup</th>
</tr>
</thead>
<tbody>
<tr>
<td>Input Impedance</td>
<td>&gt;10GΩ</td>
<td>&gt;100MΩ</td>
</tr>
<tr>
<td>Input Bias Current</td>
<td>&lt;0.1nA</td>
<td>&lt;0.15pA</td>
</tr>
<tr>
<td>Input Voltage Noise*</td>
<td>&lt;50nV p-p</td>
<td>&lt;10μV p-p</td>
</tr>
</tbody>
</table>

*0.1Hz to 10Hz bandwidth

2.7.3 Equivalent Input Circuits

Figure 2-26 compares the input circuits for the low and high resistivity setups. With the high resistivity setup, the signal is routed through the buffer amplifier, which also provides the guard for the input circuit. In contrast, the signal completely bypasses the buffer amplifier in the low resistivity mode; however, the guard is is still driven by the buffer amplifier.

![Figure 2-26. Low and High Resistivity Equivalent Circuits](image-url)
2.7.4 Resistivity Setup Criteria

The setup used depends primarily on the resistivity of the sample under test. The following discussion covers general aspects and criteria. Some experimentation may help to obtain the best choice with a particular sample.

For sample resistances above 1MΩ, accuracy degradation because of input loading comes into play. Thus, you should always use the high-resistivity setup when measuring samples above 1MΩ. For lower sample resistances, however, the best configuration to use is a compromise between noise and gain error performance, as we will now discuss.

Figure 2-27 depicts gain error versus actual sample resistance (calculated from signal voltage and sourced current). The errors shown do not include possible errors caused by current source uncertainty—only voltage measurement errors are shown.

Total measurement uncertainty is the sum of gain error and noise uncertainty. If we ignore noise uncertainty for the moment, we see from Figure 2-27 that the obvious choice for all samples is the high-resistivity setup. In fact, you may wish to operate the Model 7065 exclusively in the high-resistivity mode if slight errors caused by noise are tolerable (Hall voltages >5mV will yield error due to noise uncertainty of <0.2%).

Figure 2-28 illustrates measured noise voltage versus actual sample resistance. The tangential lines are theoretical limits given the thermal (Johnson) noise resistance. Obviously, this limit is improved (for noise due to sample resistivity) if the sample temperature is reduced to liquid nitrogen levels (77°K or below). Below sample resistances of 10kΩ, however, lowering the temperature will not significantly improve noise performance because instrument noise dominates at room temperature.

In the resistance range of 10⁴ - 10⁹Ω, Figure 2-27 shows that, for the low-resistivity setup, best accuracy is achieved if the signal voltage (sourced current, I X R) remains under 3V. There is another very important reason for doing so: the power dissipated in the sample (V²/R) will then remain under 1mW, minimizing problems caused by self-heating and temperature coefficients of the sample.

The signal voltage generated during Hall voltage measurements is usually very small—typically 20mV or less. If a sample or measurement situation results in very small Hall voltages (<5mV), and the sample resistance is less than 1MΩ, it will be advantageous to use the low-resistivity setup. Hall voltages as low as 2-4µV can be measured with less than 2% uncertainty in this mode by using a Model 181 Nanovoltmeter in place of the Model 196 DMM.
Figure 2-27. Gain Error, LO vs HI Resistivity Setup
Figure 2-28. Noise Performance, LO vs HI Resistivity Setup
2.8 FRONT PANEL SCANNER PROGRAMMING

The following paragraphs discuss front panel operation of the Models 705 and 706 Scanners in general terms. For more complete information, refer to the applicable scanner instruction manual.

2.8.1 Entering the Matrix Mode

Since the Model 7065 is organized into a 5 X 4 matrix (see Figure 2-17), the scanner must be setup to operate in the matrix mode as follows:

1. Press PRGM, 6 in sequence to enter the pole selection program. The unit will display the following message:

   POLE

2. Press 0, ENTER to select the matrix mode.
3. The unit will then display the status of column 1, row 1. Note that all crosspoints will be open after using Program 6.

2.8.2 Scanner Display Formats

Model 705 Format

In the matrix mode, the Model 705 display format appears as follows

   nn m O or C

Where: nn represents the column number
m is the row
O and C represent open and closed respectively.

Since the Model 705 can hold two cards, and up to five units can be daisy chained, the total number of possible columns is 50. Table 2-5 summarizes column numbers by card for unit 1, and Table 2-6 lists column numbers by unit. Since column numbers can be confusing, it is recommended that the Model 7065 be used in the card 1 (upper slot), unit 1 location to simplify programming.

Table 2-5. Model 705 Unit 1 Column Summary by Card

<table>
<thead>
<tr>
<th>Card</th>
<th>Model 7065 Column</th>
</tr>
</thead>
<tbody>
<tr>
<td>1 (top slot)</td>
<td>01 02 03 04 05</td>
</tr>
<tr>
<td>2 (bottom slot)</td>
<td>06 07 08 09 10</td>
</tr>
</tbody>
</table>

Table 2-6. Model 705 Column Assignments

<table>
<thead>
<tr>
<th>Unit*</th>
<th>Columns</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Card 1</td>
</tr>
<tr>
<td>1</td>
<td>1-5</td>
</tr>
<tr>
<td>2</td>
<td>11-15</td>
</tr>
<tr>
<td>3</td>
<td>21-25</td>
</tr>
<tr>
<td>4</td>
<td>31-35</td>
</tr>
<tr>
<td>5</td>
<td>41-45</td>
</tr>
</tbody>
</table>

*2 cards per unit

Model 706 Format

The Model 706 display format is similar to that of the Model 705 except that an additional digit of column information is displayed:

   nnn m o or c

Where: nnn represents the column number
m is the row number
o or c is the status of the crosspoint (open or closed)

Since each Model 706 has 10 card slots, and up to five units can be daisy chained, the total number of columns is 250. Table 2-7 lists column numbers by card for unit 1, and Table 2-8 summarizes column numbers by unit. Again, the unit should be placed in slot 1, unit 1 where possible to simplify programming.
Table 2-7. Model 706 Unit 1 Column Summary by Card

<table>
<thead>
<tr>
<th>Card</th>
<th>1</th>
<th>2</th>
<th>3</th>
<th>4</th>
<th>5</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>001</td>
<td>002</td>
<td>003</td>
<td>003</td>
<td>005</td>
</tr>
<tr>
<td>2</td>
<td>006</td>
<td>007</td>
<td>008</td>
<td>009</td>
<td>010</td>
</tr>
<tr>
<td>3</td>
<td>011</td>
<td>012</td>
<td>013</td>
<td>014</td>
<td>015</td>
</tr>
<tr>
<td>4</td>
<td>016</td>
<td>017</td>
<td>018</td>
<td>019</td>
<td>020</td>
</tr>
<tr>
<td>5</td>
<td>021</td>
<td>022</td>
<td>023</td>
<td>024</td>
<td>025</td>
</tr>
<tr>
<td>6</td>
<td>026</td>
<td>027</td>
<td>028</td>
<td>029</td>
<td>030</td>
</tr>
<tr>
<td>7</td>
<td>031</td>
<td>032</td>
<td>033</td>
<td>034</td>
<td>035</td>
</tr>
<tr>
<td>8</td>
<td>036</td>
<td>037</td>
<td>038</td>
<td>039</td>
<td>040</td>
</tr>
<tr>
<td>9</td>
<td>041</td>
<td>042</td>
<td>043</td>
<td>044</td>
<td>045</td>
</tr>
<tr>
<td>10</td>
<td>046</td>
<td>047</td>
<td>048</td>
<td>049</td>
<td>050</td>
</tr>
</tbody>
</table>

Table 2-8. Model 706 Column Assignments

<table>
<thead>
<tr>
<th>Unit*</th>
<th>Columns</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>1-50</td>
</tr>
<tr>
<td>2</td>
<td>51-100</td>
</tr>
<tr>
<td>3</td>
<td>101-150</td>
</tr>
<tr>
<td>4</td>
<td>151-200</td>
</tr>
<tr>
<td>5</td>
<td>201-250</td>
</tr>
</tbody>
</table>

*10 cards per unit

2.9 IEEE-488 BUS PROGRAMMING

The Model 7065 may be controlled over the IEEE-488 bus through the host scanner, as described in the following paragraphs. For more detailed bus information, refer to the Model 705 or 706 Instruction Manual.

2.9.1 Matrix Bus Commands

Table 2-9 summarizes the commands necessary to put the scanner in the matrix mode, and to close, open, and display crosspoints. The commands are further defined below.

Table 2-9. IEEE-488 Bus Command Summary

<table>
<thead>
<tr>
<th>Command</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>A0</td>
<td>Select matrix mode</td>
</tr>
<tr>
<td>Bnn:m*</td>
<td>Display crosspoint nn,m (705)</td>
</tr>
<tr>
<td>Bnnn:m*</td>
<td>Display crosspoint nnn,m (706)</td>
</tr>
<tr>
<td>Cnn:*</td>
<td>Close crosspoint nn,m (705)</td>
</tr>
<tr>
<td>Cnnn:*</td>
<td>Close crosspoint nnn,m (706)</td>
</tr>
<tr>
<td>Nnn:*</td>
<td>Open crosspoint nn,m (705)</td>
</tr>
<tr>
<td>Nnnn:*</td>
<td>Open crosspoint nnn,m (706)</td>
</tr>
<tr>
<td>R0</td>
<td>Open all crosspoints, display 1, 1</td>
</tr>
</tbody>
</table>

*Use of colon is optional.

2.8.3 Crosspoint Programming

Use the basic procedure below to display and program crosspoints.

1. Use Program 6 to select the matrix mode (pole 0) as described in paragraph 2.8.1.
2. Display the desired crosspoint either by using the CHANNEL button for sequential access, or by keying in the column and row numbers with the DATA keys. For example, to display column 5, row 2 on the Model 705, press the following: 0, 5, 2.
3. Once the desired crosspoint is displayed, use the OPEN or CLOSE button to update its status, as required.
4. Repeat steps 2 and 3 for all crosspoints to be changed.
5. To open all crosspoints simultaneously, and return the display to column 1, row 1, press the RESET button.

A0X: Sending this command places the scanner in the matrix mode, opens closed crosspoints, and displays column 1, row 1.

Display (B), close (C), and open (N): These commands perform the indicated operation on the crosspoint specified by the associated parameters. The Model 705 parameter format is:

```
nn:m
```

Where: nn specifies the column
m specifies the row
: delimits the parameters (optional).
Note that the nn and m values are the same as those used when programming the unit from the front panel (see paragraph 2.8). For example, to close column 2, row 1, send the command string C02:1X (or simply C021X without the optional colon delimiter).

The parameter format for the Model 706 is similar and appears as follows:

\[
\text{nnn:m}
\]

Where: nnn indicates the column  
m specifies the row  
: delimits the column and row parameters (optional).

Again, nnn and m have the same values as when programming the unit over the front panel. For example, to open column 5, row 4, send the command string N005:4X (or simply N0054X without the optional colon delimiter).

R0X: Sending this command opens all crosspoints simultaneously and displays column 1, row 1.

### 2.9.2 Example Programs

The example programs below demonstrate basic programming procedures by setting up the unit for the matrix mode and then closing crosspoints 1,1; 2,2; 3,3; and 4,4. Following operator input, the program then opens all closed crosspoints by sending the R0 command.

The programs are written in Hewlett-Packard Model 85 BASIC. The programming syntax for other similar computers (such as the HP9816) is virtually identical. See the computer programming manual for details.

Note that the programs assume that the Model 7065 is installed in the card 1 slot of unit 1 (daisy chained units).

#### Model 705 Program

<table>
<thead>
<tr>
<th>Program</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>10 REMOTE 717</td>
<td>! Put 705 in remote.</td>
</tr>
<tr>
<td>20 OUTPUT 717; &quot;A0X&quot;</td>
<td>! Put 705 in matrix mode.</td>
</tr>
<tr>
<td>30 OUTPUT 717; &quot;CO1:1X&quot;</td>
<td>! Close 1,1.</td>
</tr>
<tr>
<td>40 OUTPUT 717; &quot;CO2:2X&quot;</td>
<td>! Close 2,2.</td>
</tr>
<tr>
<td>50 OUTPUT 717; &quot;CO3:3X&quot;</td>
<td>! Close 3,3.</td>
</tr>
<tr>
<td>60 OUTPUT 717; &quot;CO4:4X&quot;</td>
<td>! Close 4,4.</td>
</tr>
<tr>
<td>70 DISP &quot;PRESS 'CONT' TO OPEN CROSSPOINTS&quot;</td>
<td></td>
</tr>
<tr>
<td>80 PAUSE</td>
<td></td>
</tr>
<tr>
<td>90 OUTPUT 717; &quot;R0X&quot;</td>
<td>! Open all crosspoints.</td>
</tr>
<tr>
<td>100 END</td>
<td></td>
</tr>
</tbody>
</table>

#### Model 706 Program

<table>
<thead>
<tr>
<th>Program</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>10 REMOTE 718</td>
<td>! Put 706 in remote.</td>
</tr>
<tr>
<td>20 OUTPUT 718; &quot;A0X&quot;</td>
<td>! Put 706 in matrix mode.</td>
</tr>
<tr>
<td>30 OUTPUT 718; &quot;CO1:1X&quot;</td>
<td>! Close 1,1.</td>
</tr>
<tr>
<td>40 OUTPUT 718; &quot;CO2:2X&quot;</td>
<td>! Close 2,2.</td>
</tr>
<tr>
<td>50 OUTPUT 718; &quot;CO3:3X&quot;</td>
<td>! Close 3,3.</td>
</tr>
<tr>
<td>60 OUTPUT 718; &quot;CO4:4X&quot;</td>
<td>! Close 4,4.</td>
</tr>
<tr>
<td>70 DISP &quot;PRESS 'CONT' TO OPEN CROSSPOINTS&quot;</td>
<td></td>
</tr>
<tr>
<td>80 PAUSE</td>
<td></td>
</tr>
<tr>
<td>90 OUTPUT 718; &quot;R0X&quot;</td>
<td>! Open all crosspoints.</td>
</tr>
<tr>
<td>100 END</td>
<td></td>
</tr>
</tbody>
</table>

### 2.10 MEASUREMENT CONSIDERATIONS

Many of the measurements made with the Model 7065 are performed on high resistivity devices and at very low current levels. At these levels, a number of factors can affect a measurement. The following paragraphs discuss some considerations when making such measurements.
2.10.1 Input Voltage Levels

The input voltage operating range for the SAMPLE INPUTS is limited to ±8V for both high and low resistivity setups. Applying a voltage greater than ±8V but less than ±12V will not damage the card, but accuracy of the measurement will suffer.

**CAUTION**
The maximum input overload is ±12V; exceeding this value may cause damage to the Model 7065.

The voltage developed across the sample will depend upon both the excitation current and the resistivity of the sample. If the sample resistance is known, you can calculate whether or not a given current will overload the inputs by using Ohm’s law:

\[ E = IR \]

For example, the voltage developed across a 100kΩ sample with a 100μA current is:

\[ E = (100 \times 10^{-6}) \times (100 \times 10^5) \]
\[ E = 10V \]

In this case, the developed voltage is above the 8V input operating range of the Hall card. The solution, of course, is to lower the current so that the voltage is within the required range.

2.10.2 Leakage Resistance

At lower resistance levels, the effects of leakage resistance are seldom a consideration because any leakage resistance is generally much higher than the resistance levels in the sample itself. With high-resistivity samples, however, leakage resistance can have a detrimental effect on the measurement. Such leakage resistance can occur in the sample test fixture, the connecting cables, or even at the Model 7065 sample input connectors, especially if these connectors are not kept clean.

To demonstrate how leakage resistance can affect measurement accuracy, let us review the equivalent circuit shown in Figure 2-29. Here \( I_s \) and \( R_s \) represent the source current and sample resistance, while the leakage resistance is \( R_L \).

\( E_s \) is the voltage developed across the sample resistance by the source current, \( I_s \) (\( E_s = I_s \times R_s \)). \( E_m \) represents the actual input voltage appearing at the device input of the Model 7065.

\( R_s \) and \( R_L \) form a voltage divider that attenuates the input signal as follows:

\[ E_m = \frac{E_s \times R_L}{E_s + R_L} \]

Since the Model 7065 has guarded sample inputs, the effects of leakage resistance are minimized. However, in extreme cases where samples with resistance in the GΩ or TΩ range are being measured, other steps may be necessary to further reduce leakage resistance effects as well. The most obvious remedy is to make sure that the leakage resistance is as high as possible. Use only good quality cables (included), and make these cables as short as possible. Also, it is important that the sample under test, test fixture, and all connectors are kept free of contamination.

---

**Figure 2-29. Leakage Resistance Effects**
2.10.3 Input Loading

When sample resistance becomes a large percentage of the Hall card input resistance, loading effects can also come into play. The effects are essentially the same as for leakage resistance; in fact, we can use the model in Figure 2-29 with the leakage resistance, $R_L$, replaced by the card input resistance, $R_i$. We can then determine the percent error for a given sample resistance as follows:

$$\% Error = \frac{R_S}{R_s + R_i} \times 100$$

For example, assume we are measuring a sample with an equivalent resistance of 100MΩ with the hall card in the low resistivity setup. Under these conditions, the card has a nominal input resistance of 10GΩ and the Model 196 has a nominal input resistance of 1GΩ (300mV, 3V ranges), yielding an equivalent resistance of 909.1MΩ. Thus, the error in this case would be:

$$\% Error = \frac{100M\Omega}{100M\Omega + 909.1M\Omega} \times 100$$

$$\% Error = 9.9\%$$

However, assume we make the same measurement with the Hall card configured for high resistivity, in which case the input resistance increases to >100Ω. The error caused by input loading is now reduced to:

$$\% Error = \frac{100M\Omega}{100M\Omega + 100\Omega} \times 100$$

$$\% Error = 0.000099\%$$

Here we see the importance of choosing the right resistivity setup for the sample being measured.

2.10.4 Input Capacitance

Virtually any circuit has at least some small amount of distributed capacitance that can slow down the response time of high impedance measurements. Even if the test sample itself has minimal capacitance, cable or Model 7065 card input capacitance effects can be noticeable, especially if current input guarding is not used.

To demonstrate input capacitance effects, consider the equivalent circuit shown in Figure 2-30. Here, $I_s$ and $R_s$ represent the sourced current and sample resistance, respectively. The voltage developed across the sample resistance as a result of the source current, $I_s$, is given as $E_s$. The input capacitance is $C_i$, and $E_m$ represents the voltage appearing at the Hall card sample input.

![Figure 2-30. Input Capacitance Effects](image)

When the source current, $I_s$, is first applied, the voltage, $E_s$, across the capacitance (and thus at the sample input) does not instantaneously rise to its final value. Instead, $C_i$ charges exponentially through $R_s$ as follows:

$$E_m = E_s \left(1 - e^{-\frac{t}{RC}}\right)$$

Here, for convenience, we can specify $R_s$ in megohms, $C_i$ in microfarads, and $t$ in seconds.

Because of $C_i$ charging, the sample input follows the exponential curve shown in Figure 2-31. At the end of one
time constant \((R\cdot C)\), the voltage reaches approximately 63% of final value. At the end of two time constants, the voltage reaches about 86% of final value, and so on. Generally, at least five time constants \((5R\cdot C)\) must be allowed for better than 1% accuracy (2-3 time constants per decade of accuracy improvement).

![Exponential Response of Input Voltage](image)

**Figure 2-31. Exponential Response of Input Voltage**

The amount of time that must be allowed will, of course, depend on the relative \(R\) and \(C\) values. For example, with a sample resistance of 100\(\Omega\) and an input capacitance of 2pF (typically, guarding the input reduces effective capacitance to 2pF or less), a time constant of 200msec results. Thus, at least one second must be allowed to achieve a better than 1% accuracy figure.

We can easily calculate percentage error figures for various \(RC\) time constants. Table 2-10 summarizes voltage ratio values and percentage error values for ten different time constants, where \(\tau = R\cdot C\).

**Table 2-10. Voltage and Percent Error For Various Time Constants**

<table>
<thead>
<tr>
<th>Time*</th>
<th>(V_M)</th>
<th>%Error</th>
</tr>
</thead>
<tbody>
<tr>
<td>(\tau)</td>
<td>0.632 (E_s)</td>
<td>36 %</td>
</tr>
<tr>
<td>(2\tau)</td>
<td>0.86 (E_s)</td>
<td>14 %</td>
</tr>
<tr>
<td>(3\tau)</td>
<td>0.95 (E_s)</td>
<td>5 %</td>
</tr>
<tr>
<td>(4\tau)</td>
<td>0.982 (E_s)</td>
<td>1.8 %</td>
</tr>
<tr>
<td>(5\tau)</td>
<td>0.993 (E_s)</td>
<td>0.647%</td>
</tr>
<tr>
<td>(6\tau)</td>
<td>0.9975 (E_s)</td>
<td>0.25 %</td>
</tr>
<tr>
<td>(7\tau)</td>
<td>0.999 (E_s)</td>
<td>0.09 %</td>
</tr>
<tr>
<td>(8\tau)</td>
<td>0.99966E(_s)</td>
<td>0.033%</td>
</tr>
<tr>
<td>(9\tau)</td>
<td>0.9999 (E_s)</td>
<td>0.012%</td>
</tr>
<tr>
<td>(10\tau)</td>
<td>0.99995E(_s)</td>
<td>0.005%</td>
</tr>
</tbody>
</table>

*\(\tau = R\cdot C\)

2.10.5. Electrostatic Interference

Electrostatic interference occurs when an electrically charged object is brought near an uncharged object, thus inducing a charge on the previously uncharged object. At lower impedance levels, the effects of electrostatic action are not noticeable because the low impedance levels allow the induced charge to dissipate quickly. At the high impedance levels of many Model 7065 measurements, however, these charges do not readily dissipate, and erroneous or erratic measurements may result. These unstable readings may be caused in two ways:

1. A DC or slowly-varying electrostatic field.
2. AC electrostatic fields which can cause errors by driving the buffer amplifier into saturation, or through rectification that produces DC errors.

Electrostatic interference is first recognizable when hand or body movements near the sample cause fluctuations in the measurements. Pickup from AC fields can be verified by checking the buffer amplifier outputs with an oscilloscope. The presence of line frequency signals on the outputs are an indication that electrostatic interference is present.

Means of minimizing electrostatic interference include:

1. Shielding. Possibilities include: a shielded room and properly shielding the test fixture.
2. Reduction of electrostatic fields. Moving power lines or other sources away from the test setup reduces the amount of electrostatic interference induced into the experiment.

2.10.6 RFI

RFI (Radio Frequency Interference) is a general term frequently used to describe electromagnetic interference over a wide range of frequencies across the spectrum. RFI can be especially troublesome at low signal levels with low-resistivity samples, but it can also affect high level measurements in extreme cases.

RFI can be caused by steady-state sources such as TV or radio broadcast signals, or it can be caused by impulse sources, as in the case of high-voltage arcing. In either case, the effect on the measurement can be considerable if enough unwanted signal is present. The effects of RFI can often be seen as an unusually large offset, or, in the case of impulse source, as sudden, erratic variations in the measurement.
RFI can be minimized by taking one or more of several precautions when making Hall measurements in such electromagnetic environments. The most obvious method is to keep all the instruments and sample as far away as possible from the RFI source. Shielding the experiment, instruments, and test leads will often reduce the interference to an acceptable level. In extreme cases, a specially constructed screen room may be necessary to sufficiently attenuate the troublesome signal.

If all else fails, external filtering of the device input paths may be required. In some cases, a simple one-pole filter may be sufficient; in more difficult cases, multiple-pole notch, or band-stop filters, tuned to the offending frequency range, may be required. Keep in mind, however, that such filtering may have other detrimental effects (such as increased response time) on the measurement.

2.10.7 Ground Loops

Ground loops that occur in multiple-instrument test setups can create error signals that cause erratic or erroneous measurements. The configuration shown in Figure 2-32 can introduce errors in two ways. Large ground currents flowing in one of the wires will encounter small resistances, either in the wires, or at the contact points. These small resistances result in voltage drops that can affect the measurement. Even if the ground loop currents are small, magnetic flux cutting across the large loops formed by ground leads can induce sufficient voltages to disturb sensitive measurements.

To prevent ground loops, instrument signal grounds should be connected to earth ground only at a single point, as shown in Figure 2-33. It is important to assure that no supply or power line currents flow through signal grounds. Sometimes, experimentation can help determine the best arrangement.

2.10.8 Offsets

Offsets generated by thermals or other factors can degrade measurement accuracy, particularly at low signals levels. Such offsets are normally nulled out as part of the measurement process, but some precautions can be taken to reduce these effects to inconsequential levels.

Offsets can generally be attributed to one or more of three sources:

1. Thermal EMFs generated at relay or connector contacts.
2. Buffer amplifier voltage offsets.
3. Offsets inherent in the measuring instruments (picoammeter or voltmeter).

Minimizing Offsets by Measurement Averaging

In most cases, an averaging measurement scheme is used to reduce the effects of such offsets. In the example measurement shown in Figure 2-34, averaging is achieved by taking the measurement twice—once each with positive and negative current. In (a), the generated voltage \( V_1 \) has the same sign as the offset voltage, \( V_{ox} \). However, by reversing the current as in the case of (b), the generated voltage \( V_2 \) now has the opposite polarity, although the offset voltage polarity is the same as before.
Once the voltages are known, the resistance can be calculated as follows:
\[
R = \frac{(V_1 + V_{os}) - (-V_2 + V_{os})}{2I}
\]

Where: \( R \) = calculated resistance  
\( V_1 \) = measured voltage with positive current  
\( V_2 \) = measured voltage with negative current  
\( I \) = applied current  
\( V_{os} \) = combined offset voltage

If we rearrange the above equation and combine terms, we have:
\[
R = \frac{V_1 + V_2}{2I}
\]

Note that the offset term, \( V_{os} \), cancels out of the equation, demonstrating the nulling effect of this measurement technique.

**Minimizing Offsets when Averaging is not Used**

The measurement scheme is more than adequate for nulling offsets in most situations. There are several other precautions that can be taken to minimize offset effects if for some reason the averaging technique is not used.

1. Make sure that the buffer amplifier offsets are properly nulled, as discussed in Section 4. Note that this consideration applies only when using the high resistivity setup because the buffers are not used in the low-resistivity mode.
2. Use the zero or relative features on the measuring instruments to null their offsets. Typically, these functions should be enabled with the sample connected and all necessary crosspoints in the measurement path closed, but with no current applied to the sample.
3. Use the maximum current possible without heating up the sample excessively. For a given sample resistance, the higher the current, the higher the voltage, reducing offset effects. Some compromise between low offsets and minimum sample heating may be necessary when choosing a current value.

**Minimizing Offset Drift**

Offset drift, which can be cause by such factors as temperature variation, can also affect measurement accuracy. In order to minimize offset drift, operate the Model 7065 and the measurement instruments in a stable temperature environment. Also, time between measurements should be as short as possible (keeping in mind other constraints such as circuit settling times).

![Offset Cancellation Diagram](image-url)

*Figure 2-34. Offset Cancellation*
SECTION 3
APPLICATIONS

3.1 INTRODUCTION

This section briefly discusses Hall effect, Hall conventions, and gives some typical measurement examples for the Model 7065. This information is intended as an overview on methods for using the Model 7065 and associated instruments for measurements. References are included for more detailed information on making van der Pauw resistivity and Hall effect voltage measurements.

Information in this section includes:

3.2 Recommended Equipment: Summarizes the equipment necessary for a complete system.

3.3 Hall Effect Conventions and Principles: Covers Hall and terminal conventions used in this manual, and discusses basic Hall effect principles.

3.4 Van der Pauw Resistivity Measurements: Covers the basic procedures for determining the resistivity of samples.

3.5 Hall Voltage Measurements: Details methods to measure and calculate Hall coefficients and Hall mobility.

3.6 Measuring Bar Type Samples: Discusses the procedures necessary for measuring the Hall voltage and resistivity of bar type samples.

3.7 Measuring Bridge and Parallelepiped Type Specimens: Outlines basic test procedures for both 6- and 8-contact samples.

3.8 Sample Resistivity and Hall Voltage Programs: Lists sample programs that can be used to measure and calculate Hall coefficients and resistivities of typical samples.

3.2 RECOMMENDED EQUIPMENT

Table 3-1 summarizes the equipment necessary for a complete Hall measurement system. The Models 196, 220, 485, 705, and 7065 are available from Keithley Instruments, while the remainder of the equipment must be obtained from other sources.

<table>
<thead>
<tr>
<th>Equipment</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>Model 196 DMM</td>
<td>Measure sample voltages</td>
</tr>
<tr>
<td>Model 220 Current Source</td>
<td>Supply sample current</td>
</tr>
<tr>
<td>Model 485 Picoammeter</td>
<td>Measure sample current</td>
</tr>
<tr>
<td>Model 705 or 706 Scanner</td>
<td>Control Hall Effect card</td>
</tr>
<tr>
<td>Cryostat</td>
<td>Set sample temperature</td>
</tr>
<tr>
<td>Magnet</td>
<td>Apply magnetic field</td>
</tr>
<tr>
<td>Magnet power supply</td>
<td>Supply magnet power</td>
</tr>
<tr>
<td>Gauss meter</td>
<td>Used to measure actual magnetic field strength</td>
</tr>
</tbody>
</table>

3.3 HALL EFFECT CONVENTIONS AND PRINCIPLES

The following paragraphs discuss Hall effect sign and sample terminal conventions, as well as basic Hall effect principles.

3.3.1 Hall Effect Sign Conventions

Figures 3-1 and 3-2 show the sign conventions for n-type and p-type materials respectively. Table 3-2 summarizes generally accepted units of measure.

<table>
<thead>
<tr>
<th>Quantity</th>
<th>Symbol</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Sample dimensions</td>
<td>L, t, w, d</td>
<td>cm</td>
</tr>
<tr>
<td>Potential difference</td>
<td>V</td>
<td>V</td>
</tr>
<tr>
<td>Charge</td>
<td>e, q</td>
<td>C</td>
</tr>
<tr>
<td>Carrier concentration</td>
<td>n, p</td>
<td>cm^{-3}</td>
</tr>
<tr>
<td>Drift mobility</td>
<td>µ</td>
<td>cm^{2}/V's</td>
</tr>
<tr>
<td>Hall mobility</td>
<td>µH</td>
<td>cm^{2}/V's</td>
</tr>
<tr>
<td>Current density</td>
<td>J</td>
<td>A/cm^{2}</td>
</tr>
<tr>
<td>Hall coefficient</td>
<td>R_H</td>
<td>cm^{3}/C</td>
</tr>
<tr>
<td>Electric field</td>
<td>E</td>
<td>V/cm</td>
</tr>
<tr>
<td>Magnetic flux density</td>
<td>B</td>
<td>gauss</td>
</tr>
<tr>
<td>Resistivity</td>
<td>ρ</td>
<td>Ω-cm</td>
</tr>
</tbody>
</table>
In the case of van der Pauw resistivity measurements, a current is applied between two terminals, while the voltage is measured between the two opposite terminals, as shown in Figure 3-3. A total of eight such measurements are taken with each possible terminal and current convention. In this manual, these voltages are designated $V_1$ through $V_8$, and are covered in detail in paragraph 3.4.

The connections for Hall voltage measurements are shown in Figure 3-4. Here, current is applied and voltage is measured across the diagonal of the sample. Again eight such measurements are necessary, both with positive and negative current, and positive and negative magnetic flux (often additional measurements are taken with no flux applied). Again, these voltages are designated $V_1$ through $V_8$, as discussed in paragraph 3.5.

Conventions used for bar and bridge specimens are similar, and are covered in paragraphs 3.6 and 3.7 respectively.

### 3.3.3 Basic Hall Effect Principles

For the following discussion, refer to the basic sample configuration shown in Figure 3-1.

If a current, $I = J/A$, is applied across the length of the sample, a voltage is developed across that sample. If a magnetic field, $B$, is subsequently applied normal (perpendicular) to the current, the carriers are displaced from their normal paths, and a voltage, $E_H$, is developed. This voltage is known as the Hall voltage.

Once the Hall voltage is known, the Hall coefficient can be defined as follows:

$$R_H = \frac{E_H}{JB} = \frac{E_H}{qnvB}$$

Where: $R_H =$ Hall coefficient  
$E_H =$ Hall voltage  
$J =$ current density  
$B =$ magnetic field  
$n =$ carrier concentration  
$q =$ carrier charge  
$v =$ drift mobility

Once the Hall coefficient is known, carrier concentration and mobility can be calculated when used with the measured resistivity.
Figure 3-3. Resistivity Measurement Conventions
Figure 3-4. Hall Voltage Measurement Conventions
3.4 VAN DER PAUW RESISTIVITY MEASUREMENTS

The following paragraphs describe basic test configuration, test procedures, and calculations necessary to make resistivity measurements using the van der Pauw method.

3.4.1 Test Configuration

Figure 3-5 shows the basic test configuration for making resistivity measurements. A detailed connection diagram may be found in Section 2.

3.4.2 Test Procedure

Use the following general procedure to measure parameters necessary to calculate sample resistivity. The procedure assumes that the sample has been stabilized at the desired operating temperature and will remain at that temperature throughout the tests.

1. Turn on all the instruments and allow them to warm up for the prescribed period for rated accuracy.
2. Place the Models 196 and 485 in autoranging. Be sure the Model 196 is in the DCV function.
3. Using front panel Program 6, set the Model 705 Scanner to the matrix mode.
4. Program crosspoint 5,4 to select low or high resistivity. This crosspoint should be open for low resistivity, and it should be closed for high resistivity.
5. Program the Model 220 current to the desired value in the range of 500fA to 100mA. The maximum current that can be used will depend on the resistance of the sample; remember that the maximum Model 7065 input voltage is ±8V. In order to maintain proper sign convention for the measured voltage and to minimize common-mode errors, program only positive currents.
6. Close the crosspoints necessary to measure V, as indicated in Table 3-3. Zero the DMM and enable the REL function on the Model 485.
7. Turn on the 220 output by pressing the OPERATE key.
8. Measure V1 by noting the reading on the Model 196. Also note the current measured by the Model 485 Picoammeter.
9. Turn off the Model 220 output and open the crosspoints.
10. Re-zero the DMM, enable REL on the Model 485, then turn on the Model 220 output.
11. Measure and record the remaining voltages (V2 through V5) listed in Table 3-3 by closing the appropriate crosspoints. Be sure to open the crosspoints from the previous measurement before closing crosspoints for the present measurement.

<table>
<thead>
<tr>
<th>Voltage Designation</th>
<th>Crosspoints Closed (Column, Row)*</th>
<th>Current Applied Between</th>
<th>Voltage Measured Between</th>
</tr>
</thead>
<tbody>
<tr>
<td>V1</td>
<td>1,2 2,1 3,3 4,4</td>
<td>1-2</td>
<td>3-4</td>
</tr>
<tr>
<td>V2</td>
<td>2,2 1,1 3,3 4,4</td>
<td>2-1</td>
<td>3-4</td>
</tr>
<tr>
<td>V3</td>
<td>2,2 1,3 3,4 4,1</td>
<td>2-3</td>
<td>4-1</td>
</tr>
<tr>
<td>V4</td>
<td>2,3 1,2 3,4 4,1</td>
<td>3-2</td>
<td>4-1</td>
</tr>
<tr>
<td>V5</td>
<td>2,3 1,4 3,1 4,2</td>
<td>3-4</td>
<td>1-2</td>
</tr>
<tr>
<td>V6</td>
<td>2,4 1,3 3,1 4,2</td>
<td>4-3</td>
<td>1-2</td>
</tr>
<tr>
<td>V7</td>
<td>2,4 1,1 3,2 4,3</td>
<td>4-1</td>
<td>2-3</td>
</tr>
<tr>
<td>V8</td>
<td>2,1 1,4 3,2 4,3</td>
<td>1-4</td>
<td>2-3</td>
</tr>
</tbody>
</table>

*Only those crosspoints shown can be closed for a specific measurement, except 5,4 which controls input configuration for low or high-resistivity samples.
Figure 3-5. Measurement Configuration for Resistivity and Hall Voltage Measurements
3.4.3 Resistivity Calculations

Once the voltages and current through the sample have been measured, the resistivity can be calculated as follows. Two values of resistivity, \( p_a \) and \( p_b \), are computed as follows:

\[
p_a = \frac{1.1331 \cdot f_a \cdot t_s \cdot (V_2 + V_4 - V_1 - V_3)}{I}
\]

\[
p_b = \frac{1.1331 \cdot f_b \cdot t_s \cdot (V_6 + V_8 - V_5 - V_7)}{I}
\]

Where: \( p_a \) and \( p_b \) are resistivities in ohm-cm
\( t_s \) is the sample thickness in cm
\( V_1 \text{--} V_8 \) represent the voltages measured by the Model 196 (see Table 3-3)
\( I \) is the current through the sample in amperes, as measured by the Model 485
\( f_a \) and \( f_b \) are geometrical factors based on sample symmetry, and are related to the two resistance ratios \( Q_a \) and \( Q_b \) as shown below (\( f_a=f_b=1 \) for perfect symmetry).

\( Q_a \) and \( Q_b \) can be calculated using the measured voltages from Table 3-3 as follows:

\[
Q_a = \frac{V_2 - V_1}{V_4 - V_3}
\]

\[
Q_b = \frac{V_6 - V_5}{V_8 - V_7}
\]

\( Q \) and \( f \) are related as follows:

\[
\frac{Q-1}{Q+1} = \frac{f}{\text{arc cosh} \left( \frac{0.693}{0.693} \frac{1}{e^{0.693}} \right)}
\]

A plot of this function is shown in Figure 3-6.

Note that if \( p_a \) and \( p_b \) are not within 10% of one another, the sample is not sufficiently uniform to determine resistivity.

Once \( p_a \) and \( p_b \) are known, the average resistivity, \( p_{\text{AVO}} \), can be determined as follows:

\[
p_{\text{AVO}} = \frac{p_a + p_b}{2}
\]

Figure 3-6. Plot of \( f \) vs \( Q \)

3.5 HALL VOLTAGE MEASUREMENTS

The following paragraphs discuss Hall voltage measurements on van der Pauw type samples.

3.5.1 Test Configuration

The sample test configuration shown in Figure 3-5 can be used to measure the parameters necessary to calculate Hall coefficients. Test connection details are located in Section 2. In addition to the equipment shown, a suitable magnet or electromagnet will be necessary to generate the required magnetic field.
3.5.2 Test Procedure

Use the procedure below to measure data necessary to calculate Hall coefficients. Note that the sample should be stabilized at the desired temperature before and during the tests. Also, the flux density magnitude must be kept constant during the measurements.

1. Turn on all the instruments and allow them to warm up for the prescribed period for rated accuracy.
2. Place the Models 196 and 485 in autoranging. Be sure the Model 196 is in the DCV function.
3. Using front panel Program 6, set the Model 705 Scanner to the matrix mode.
4. Program crosspoint 5,4 to select low or high resistivity. This crosspoint should be open for low resistivity, and it should be closed for high resistivity.
5. Program the Model 220 current to the desired value in the range of 500fA to 100mA. The maximum current that can be used will depend on the resistance of the sample; remember that the maximum Model 7065 input voltage is ±6V. In order to maintain proper sign convention for the measured voltage, program only positive currents.
6. Close crosspoints 2,1; 1,3; 3,4; and 4,2, then zero the Model 196 and enable the Model 485 relative function.
7. Turn on the Model 220 output by pressing the OPERATE key.
8. Turn on the magnetic field, and set it to the desired positive flux density (+B). Measure and record the value of +B.
9. Measure the voltage \( V_1 \) by noting the Model 196 reading.
10. Note and record the current being measured by the Model 485 Picoammeter.
11. Measure and record \( V_4 \) through \( V_8 \), as listed in Table 3-4 by closing the appropriate crosspoints. Be sure to open crosspoints from the previous measurement and turn off the Model 220 output. After closing the next crosspoints, re-zero the DMM and enable REL on the Model 485 before turning on the Model 220 output.
12. Reverse the magnetic flux and adjust it to the same magnitude used for positive flux; measure and record the flux value.
13. Measure \( V_5 \) through \( V_8 \) listed in Table 3-4, now with negative flux (-B).

### Table 3-4. Crosspoint Summary for Hall Voltage Measurements

<table>
<thead>
<tr>
<th>Voltage Designation</th>
<th>Flux</th>
<th>Crosspoints Closed (Column, Row)*</th>
<th>Current Applied Between</th>
<th>Voltage Measured Between</th>
</tr>
</thead>
<tbody>
<tr>
<td>( V_1 )</td>
<td>+B</td>
<td>2,1 1,3 3,4 4,2</td>
<td>1-3</td>
<td>4-2</td>
</tr>
<tr>
<td>( V_2 )</td>
<td>+B</td>
<td>2,3 1,1 3,4 4,2</td>
<td>3-1</td>
<td>4-2</td>
</tr>
<tr>
<td>( V_3 )</td>
<td>+B</td>
<td>2,2 1,4 3,1 4,3</td>
<td>2-4</td>
<td>1-3</td>
</tr>
<tr>
<td>( V_4 )</td>
<td>+B</td>
<td>2,4 1,2 3,1 4,3</td>
<td>4-2</td>
<td>1-3</td>
</tr>
<tr>
<td>( V_5 )</td>
<td>-B</td>
<td>2,1 1,3 3,4 4,2</td>
<td>1-3</td>
<td>4-2</td>
</tr>
<tr>
<td>( V_6 )</td>
<td>-B</td>
<td>2,3 1,1 3,4 4,2</td>
<td>3-1</td>
<td>4-2</td>
</tr>
<tr>
<td>( V_7 )</td>
<td>-B</td>
<td>2,2 1,4 3,1 4,3</td>
<td>2-4</td>
<td>1-3</td>
</tr>
<tr>
<td>( V_8 )</td>
<td>-B</td>
<td>2,4 1,2 3,1 4,3</td>
<td>4-2</td>
<td>1-3</td>
</tr>
</tbody>
</table>

*Only those crosspoints shown can be closed for a specific measurement, except 5,4 which controls card input configuration for low or high resistivity samples.
3.5.3 Hall Coefficient Calculations

Once the voltages are measured, two Hall coefficients, $R_{hc}$ and $R_{hd}$, can be calculated as follows:

$$R_{hc} = 2.5 \times 10^9 \frac{t_s (V_s - V_1 + V_5 - V_6)}{BI}$$

$$R_{hd} = 2.5 \times 10^9 \frac{t_s (V_4 - V_3 + V_7 - V_5)}{BI}$$

Where: $R_{hc}$ and $R_{hd}$ are Hall coefficients in cm$^2$/C
$t_s$ is the sample thickness in cm
B is the magnetic flux in gauss
I is the current measured by the Model 485 in amperes
$V_s, V_1, V_5$ are voltages measured by the Model 196 (see Table 3-4)

Note that $R_{hc}$ and $R_{hd}$ should be within 10% of one another, or the sample is not sufficiently uniform.

Once $R_{hc}$ and $R_{hd}$ have been calculated, the average Hall Coefficient, $R_{hAVG}$, can be determined as follows:

$$R_{hAVG} = \frac{R_{hc} + R_{hd}}{2}$$

3.5.4 Hall Mobility Calculation

Once the Hall coefficient and resistivity are known, the Hall mobility can be calculated as follows:

$$\mu_h = \frac{|R_{hAVG}|}{\rho_{AVG}}$$

Where: $\mu_h$ = the Hall mobility in cm$^2$/V·s
$R_{hAVG}$ = the average Hall coefficient in cm$^2$/C
$\rho_{AVG}$ = the average resistivity in ohm-cm

3.6 MEASURING BAR TYPE SAMPLES

The following paragraphs discuss procedures and test configurations for measuring resistivity and Hall voltage measurements on bar samples.

3.6.1 Test Configurations

Figures 3-7 and 3-8 show the general test configurations for making resistivity and Hall voltage measurements. The two setups are very similar except for the way the voltmeter is connected. For resistivity measurements (Figure 3-7), the voltmeter is connected between terminal 4 and 5 of the terminal block. For Hall voltage measurements, a suitable potentiometer is to be connected between terminals 4 and 5, while the voltmeter is connected between the wiper of the pot and terminal 3 of the block.

The crosspoints that must be closed depend on whether resistivity or Hall voltage measurements are to be taken. Table 3-6 summarizes which crosspoints to close for each type of measurement.
Figure 3-7. Test Configuration for Resistivity Measurements of Bar Samples
Figure 3-8. Test Configuration for Hall Voltage Measurement of Bar Samples
3.6.2 Determining Potentiometer Values

The loading effects of the DMM and potentiometer can affect the accuracy of both low and high resistivity measurements. Table 3-5 summarizes recommended potentiometer values, along with nominal errors. Additional considerations for value selection are discussed below. An equivalent circuit of the card and sample for this discussion is shown in Figure 3-9.

High Resistivity

In order to minimize loading errors, the equivalent resistance seen by the buffers must be as high as possible; also, the DMM input resistance must be substantially higher than the equivalent resistance seen at its input. The worst-case condition occurs when the potentiometer wiper is at the center of its adjustment range (R=½). For example, in the equivalent circuit of Figure 3-10, assume that Rp has a value of 200kΩ. In this case, Re = ½ (½) Rp = ¼ Rp = 50kΩ.

Low Resistivity

For the low-resistivity setup, and for values significantly less than the values listed for Re in Table 3-5, better accuracy can be obtained by using a different value for Rp than that shown in the table. Using the equivalent circuit shown in Figure 3-11, the relationship between the error, E, Rp, and R12 is given as follows:

\[
E\% \approx \frac{R_p}{4R_{in}} + \frac{R_{12}}{R_p} \times 100
\]

Where: Rp = potentiometer value
Rin = input resistance of the Model 196 (Rin = 10MΩ for ≥30V range, 10Ω for 300mV, 3V ranges)

Note that this approximation is valid for E < 10%

Table 3-5. Minimum and Maximum Potentiometer Values for Hall Bar Measurements

<table>
<thead>
<tr>
<th>Resistivity Mode</th>
<th>R12*</th>
<th>196 Range</th>
<th>Recommended Rp*</th>
<th>Additional Error Introduced by Rp (E)</th>
</tr>
</thead>
<tbody>
<tr>
<td>LO</td>
<td>Up to 1kΩ</td>
<td>30V, 300V</td>
<td>200kΩ</td>
<td>1%</td>
</tr>
<tr>
<td>LO</td>
<td>Up to 10kΩ</td>
<td>300mV, 3V</td>
<td>1MΩ</td>
<td>1%</td>
</tr>
<tr>
<td>HI</td>
<td>All</td>
<td>All</td>
<td>50kΩ</td>
<td>0.25%</td>
</tr>
</tbody>
</table>

*See Figure 3-9
Figure 3-9. Hall Bar Equivalent Circuit
This crosspoint should be open for low resistivity, and it should be closed for high resistivity.

5. Program the Model 220 current to the desired value in the range of 500μA to 100mA. The maximum current that can be used will depend on the resistance of the sample; remember that the maximum Model 7065 input voltage is ±8V. In order to maintain proper sign convention for the measured voltage, and to minimize common-mode errors, program only positive currents.

6. Close crosspoints 2, 4; 3, 1; and 4, 2 by programming the scanner. Zero the Model 196 and enable the Model 485 relative function.

7. Turn on the Model 220 output by pressing the OPERATE key.

8. Note and record the voltage (Vₐ) and (Iₐ) current readings on the Models 196 and 485.

9. Open all crosspoints by pressing the RESET button on the scanner and turn off the Model 220 output after measurements are complete.

### 3.6.4 Hall Voltage Measurements

The following procedure assumes that the sample is held stable at the desired temperature throughout the tests, and that the applied magnetic flux is also held constant at the desired density.

1. Turn on all instruments and allow them to warm up sufficiently.

2. Select the DCV function on the Model 196, and set both the Model 196 and the Model 485 to autoranging.

3. Using Program 6, set the Model 705 Scanner to the matrix mode.

4. Program crosspoint 5, 4 for the desired resistivity setup (open, low resistivity; closed, high resistivity).

5. Program the Model 220 Current Source to the desired (positive) current.

6. Program the scanner to close contacts 2, 4; 3, 1; 4, 2; and 5, 3, as summarized in Table 3-6.

7. Enable zero on the Model 196 and the relative function on the Model 485.

8. Turn on the Model 220 output by pressing the OPERATE button.

9. With no magnetic field applied, adjust the potentiometer for a reading of 0V on the Model 196.

10. Turn on the magnetic field and adjust it for the desired intensity.

11. Note and record the Model 196 voltage (Vₐ) as well as the sample current (Iₐ) measured by the Model 485 Picoammeter.

12. Open all crosspoints by pressing RESET on the scanner and turn off the Model 220 output after all measurements are completed.
Table 3-6. Crosspoint Configurations for Bar Measurements

<table>
<thead>
<tr>
<th>Measurement</th>
<th>Closed Crosspoints (Column, Row)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Resistivity</td>
<td>2,4 3,1 4,2</td>
</tr>
<tr>
<td>Hall Voltage</td>
<td>2,4 3,1 4,2 5,3</td>
</tr>
</tbody>
</table>

3.6.5 Calculations

Resistivity

The resistivity of the sample is related to the measurements as follows:

\[ V_R = \frac{p I_R}{y t} \]

Where: \( V_R \) = the voltage measured by the Model 196
\( p = \) the resistivity in ohm-cm
\( I_R = \) the current in amperes measured by the Model 485
\( t = \) the sample thickness in cm
\( x, y = \) sample dimensions shown in Figure 3-12 in cm.

Hall Voltage

The magnetic field, \( B \), will generate a Hall voltage, \( V_H \), as follows:

\[ V_H = 6.25 \times 10^{19} \frac{I_{e}B}{nt} \]

Where: \( V_H \) = the voltage measured by the Model 196 in volts
\( I_{e} = \) the current in amperes measured by the Model 485
\( B = \) the flux density in gauss
\( t = \) the sample thickness in cm
\( n = \) concentration in electrons/cm\(^3\) (substitute \( p \) for hole conduction in \( p \) type materials.

Mobility

Once the resistivity and Hall voltages are known, the mobility, \( \mu \), can be determined as follows:

\[ \mu = 10^8 \frac{x V_H}{y B V_R} \]

Where: \( \mu = \) the mobility in cm\(^2\)/V-s
\( V_H = \) the Hall voltage measured by the Model 196
\( x, y = \) sample dimensions in cm (Figure 3-12)
\( B = \) flux density in gauss
\( V_R = \) voltage measured by the Model 196

3.7 MEASURING BRIDGE AND PARALLELEPIPED TYPE SPECIMENS

The fundamental procedures for measuring 6- and 8-contact bridge and parallelepiped type specimens are discussed in the following paragraphs.

3.7.1 Test Configurations

Figure 3-13 shows the test setup for 6-contact specimens, and the setup for 8-contact samples is shown in Figure 3-14. In both cases, four sample contacts are connected to the sample inputs on the Model 7065. The current source and picoammeter must be connected directly to the sample itself. Section 2 covers physical connections in more detail.
Figure 3-12. Bar Sample Dimensions
Figure 3-13. 6-Contact Sample Connections
Figure 3-14. 8-Contact Sample Connections
3.7.2 Resistivity Measurements

Use the basic procedure below to measure parameters for 6- and 8-contact specimens. As always, the sample should be stabilized at the desired temperature both before and during the tests.

1. Turn on all the instruments and allow them to warm up for the prescribed period for rated accuracy.
2. Place the Models 196 and 485 in autoranging. Be sure the Model 196 is in the DCV function.
3. Using front panel Program 6, set the Model 705 Scanner to the matrix mode.
4. Program crosspoint 5.4 to select low or high resistivity. This crosspoint should be open for low resistivity, and it should be closed for high resistivity.
5. Program the Model 220 current to the desired positive current value in the range of 500fA to 100mA. The maximum current that can be used will depend on the resistance of the sample; remember that the maximum Model 7065 input voltage is ±8V.
6. Close the appropriate crosspoints for the sample by programming the scanner (Table 3-7 or 3-8). Zero the Model 196 and enable the Model 485 relative function.
7. Turn on the Model 220 output by pressing the OPERATE key.
8. Note and record the voltage (V1) and current readings on the Models 196 and 485.
9. Turn off the Model 220 output, open the presently closed crosspoints, and close the second set of crosspoints listed in Table 3-7 or 3-8. Enable REL on the picammeter then turn the Model 220 output back on. Measure the voltage V2.
10. Program the Model 220 for a negative current of the same magnitude as is presently programmed.

11. Note the Model 485 current reading and compare it to the one obtained in step 8. If the current magnitudes are not exactly the same, reprogram the Model 220, as necessary, so that the magnitude of the current is as close as possible to that obtained in step 8.
12. Repeat steps 6 through 11 and measure V3 and V4 by closing the crosspoints indicated in the appropriate table (Table 3-7 or 3-8).

<table>
<thead>
<tr>
<th>Voltage Designation</th>
<th>Crosspoints Closed (Column, Row)</th>
<th>Current Between</th>
<th>Voltage Between</th>
</tr>
</thead>
<tbody>
<tr>
<td>V1</td>
<td>3,2 4,1</td>
<td>1-2</td>
<td>4-6</td>
</tr>
<tr>
<td>V2</td>
<td>3,3 4,4</td>
<td>1-2</td>
<td>3-5</td>
</tr>
<tr>
<td>V3</td>
<td>3,2 4,1</td>
<td>2-1*</td>
<td>4-6</td>
</tr>
<tr>
<td>V4</td>
<td>3,3 4,4</td>
<td>2-1*</td>
<td>3-5</td>
</tr>
</tbody>
</table>

*Reverse current by programming source for opposite polarity

<table>
<thead>
<tr>
<th>Voltage Designation</th>
<th>Crosspoints Closed (Column, Row)</th>
<th>Current Between</th>
<th>Voltage Between</th>
</tr>
</thead>
<tbody>
<tr>
<td>V1</td>
<td>3,2 4,1</td>
<td>1-2</td>
<td>4-6</td>
</tr>
<tr>
<td>V2</td>
<td>3,3 4,4</td>
<td>1-2</td>
<td>5-7</td>
</tr>
<tr>
<td>V3</td>
<td>3,2 4,1</td>
<td>2-1*</td>
<td>4-6</td>
</tr>
<tr>
<td>V4</td>
<td>3,3 4,4</td>
<td>2-1*</td>
<td>5-7</td>
</tr>
</tbody>
</table>

*Reverse current by programming source for opposite polarity
3.7.3 Hall Voltage Measurements

The following procedure assumes that the sample is held stable at the desired temperature throughout the tests, and that the applied magnetic flux is also held constant at the desired flux density.

1. Turn on all instruments and allow them to warm up sufficiently.
2. Select the DCV function on the Model 196, and set both the Model 196 and the Model 485 to autoranging.
3. Using Program 6, set the Model 705 Scanner to the matrix mode.
4. Program crosspoint 5.4 for the desired resistivity (open, low resistivity; closed, high resistivity).
5. Program the Model 220 Current Source to the desired positive current.
6. Program the scanner to close the first set of contacts listed in Table 3-9 or 3-10.
7. Enable zero on the Model 196 and the relative function on the Model 485.
8. Turn on the Model 220 output by pressing the OPERATE button.
9. Turn on the magnetic flux and adjust it to the desired positive value (+B).
10. Note and record the Model 196 voltage (V₁) as well as the sample current measured by the Model 485 Picoammeter.
11. For 6-contact specimens only, measure the voltage V₂, as indicated in Table 3-9.
12. Reverse the Model 220 current polarity and note the current reading on the Model 485. If necessary, reprogram the current source so that the magnitude of the measured current is as close as possible to that measured in step 10.
13. Measure the remaining voltage(s) as indicated in Table 3-9 or 3-10 using steps 6 through 12 of the above procedure.
14. Reverse the magnetic flux and repeat steps 10 through 13.

3.7.4 Calculations

Once the necessary measurements are made, resistivity, Hall voltage, and Hall mobility calculations can be made from the data and certain sample dimensions, which are outlined in Figure 3-15.

### Table 3-9. 6-Contact Sample Hall Voltage Measurements

<table>
<thead>
<tr>
<th>Voltage Designation</th>
<th>Flux Polarity</th>
<th>Crosspoints Closed (Column, Row)</th>
<th>Current* Between</th>
<th>Voltage Between</th>
</tr>
</thead>
<tbody>
<tr>
<td>V₁</td>
<td>+B</td>
<td>3,1 4,4</td>
<td>1-2</td>
<td>6-5</td>
</tr>
<tr>
<td>V₂</td>
<td>+B</td>
<td>3,2 4,3</td>
<td>1-2</td>
<td>4-3</td>
</tr>
<tr>
<td>V₃</td>
<td>+B</td>
<td>3,1 4,4</td>
<td>2-1</td>
<td>6-5</td>
</tr>
<tr>
<td>V₄</td>
<td>+B</td>
<td>3,2 4,3</td>
<td>2-1</td>
<td>4-3</td>
</tr>
<tr>
<td>V₅</td>
<td>-B</td>
<td>3,1 4,4</td>
<td>1-2</td>
<td>6-5</td>
</tr>
<tr>
<td>V₆</td>
<td>-B</td>
<td>3,2 4,3</td>
<td>1-2</td>
<td>4-3</td>
</tr>
<tr>
<td>V₇</td>
<td>-B</td>
<td>3,1 4,4</td>
<td>2-1</td>
<td>6-5</td>
</tr>
<tr>
<td>V₈</td>
<td>-B</td>
<td>3,2 4,3</td>
<td>2-1</td>
<td>4-3</td>
</tr>
</tbody>
</table>

*Reverse current by programming source for opposite polarity

### Table 3-10. 8-Contact Sample Hall Voltage Measurements

<table>
<thead>
<tr>
<th>Voltage Designation</th>
<th>Flux Polarity</th>
<th>Crosspoints Closed (Column, Row)</th>
<th>Current* Between</th>
<th>Voltage Between</th>
</tr>
</thead>
<tbody>
<tr>
<td>V₁</td>
<td>+B</td>
<td>3,1 4,3</td>
<td>1-2</td>
<td>6-5</td>
</tr>
<tr>
<td>V₂</td>
<td>+B</td>
<td>3,1 4,3</td>
<td>2-1</td>
<td>6-5</td>
</tr>
<tr>
<td>V₃</td>
<td>-B</td>
<td>3,1 4,3</td>
<td>1-2</td>
<td>6-5</td>
</tr>
<tr>
<td>V₄</td>
<td>-B</td>
<td>3,1 4,3</td>
<td>2-1</td>
<td>6-5</td>
</tr>
</tbody>
</table>

*Reverse current by programming source for opposite polarity
Figure 3-15. Sample Dimensions Necessary for Calculations
Resistivity Calculations

Two resistivity values, $p_A$ and $p_B$, can be calculated as follows:

$$p_A = \frac{w_As}{2D_1} (V_1 - V_3)$$

and,

$$p_B = \frac{w_As}{2D_2} (V_1 - V_4)$$

Where: $p_A$ and $p_B =$ resistivity in ohm-cm
$w_s =$ sample width in cm
$t_s =$ sample thickness in cm
$D_1$ and $D_2 =$ sample dimensions (Figure 3-15) in cm
$I =$ current measured by the Model 485 in amperes
$V_1 - V_4 =$ voltages measured by the Model 196 (Table 3-7, 6-contact; Table 3-8, 8-contact.)

Once $p_A$ and $p_B$ are known, the average resistivity, $p_{AVG}$, can be computed as follows:

$$p_{AVG} = \frac{p_A + p_B}{2}$$

Note that $p_{AVG}$ is also given in ohm-cm

Hall Voltage Calculations

For 6-contact samples, two Hall coefficients, $R_{H1}$ and $R_{H2}$, can be calculated as follows:

$$R_{H1} = \frac{2.5 \times 10^7 t_s}{BI} (V_1 - V_2 + V_7 - V_8)$$

and,

$$R_{H2} = \frac{2.5 \times 10^7 t_s}{BI} (V_2 - V_4 + V_8 - V_6)$$

Where: $R_{H1}$ and $R_{H2} =$ Hall coefficients in cm$^3$/C (coulomb)
$t_s =$ sample thickness in cm
$B =$ flux density in gauss
$I =$ current in amperes measured by the Model 485
$V_1 - V_4 =$ voltages measured by the Model 196 from Table 3-9

From these two coefficients, the average Hall coefficient, $R_{H_{AVG}}$, can be computed in the following manner:

$$R_{H_{AVG}} = \frac{R_{H1} + R_{H2}}{2}$$

Note that $R_{H_{AVG}}$ is in units of cm$^3$/C

For eight contact samples, only a single coefficient, $R_H$, need be derived as follows:

$$R_H = \frac{2.5 \times 10^7 t_s}{BI} (V_1 - V_2 + V_4 - V_3)$$

Where: $R_H =$ Hall coefficient in cm$^3$/C
$t_s =$ sample thickness in cm
$B =$ flux density in gauss
$I =$ current measured by the Model 485 in amperes
$V_1 - V_4 =$ voltages from Table 3-10 measured by the Model 196.

Hall Mobility

The Hall mobility, $\mu_H$, can be computed from the Hall coefficient(s) and average resistivity of the specimen as follows:

$$\mu_H = \frac{|R_{H_{AVG}}|}{p_{AVG}}$$

Where: $\mu_H =$ Hall mobility in cm$^2$/V-s
$R_{H_{AVG}} =$ average Hall coefficient in cm$^3$/C
$p_{AVG} =$ average resistivity in ohm-cm

For 8-contact samples, substitute $R_H$ for $R_{H_{AVG}}$ in the above equation.
3.8 SAMPLE RESISTIVITY AND HALL VOLTAGE PROGRAMS

Three programs are included in this section in order to demonstrate fundamental programming techniques. Programs 1 and 5 are intended to determine the Hall coefficients using the van der Pauw method, and Programs 2 and 6 determine the resistivity of van der Pauw samples. Programs 3 and 7 measure bar sample resistivity, and programs 4 and 8 can be used to measure the resistivity of 6- or 8-contact bridge or parallelepiped samples.

Programs 1-4 are written in Hewlett-Packard Model 85 BASIC, but they can be converted to run on other computers with similar programming languages. The programming language for the Helwett-Packard 9816, for example, is almost identical to that of the Model 85. Table 3-11 summarizes important differences between the two computer programming languages.

Program 5-8 are written for an IBM PC/XT computer (and some PC compatibles) equipped with a Keithley Model 8573A IEEE-488 interface. Note that the program listings do not include lines 1-6 of the DECL.BAS file which must be added to the front of each program. See the Model 8573A Instruction Manual for details.

NOTE

These programs are intended only as examples, and they may not yield optimum accuracy with all measurement conditions.

3.8.1 Test Configurations

Figure 3-16 shows the general test configuration necessary to run Programs 1, 2, 5, and 6. The necessary test fixture, cryostat, magnet, and magnet power supply are not shown on the diagram. Figure 3-17 shows the test configuration for Programs 2 and 7, and Figure 3-18 gives the configuration for Programs 4 and 8.

Table 3-11. HP-85 and HP-9816 Programming Language Differences

<table>
<thead>
<tr>
<th>HP-85 Statement</th>
<th>HP-9816 Equivalent Statement(s)</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>CLEAR</td>
<td>C$=CHR$(255) &amp; CHR$(75)</td>
<td>Clear screen, home cursor</td>
</tr>
<tr>
<td></td>
<td>H$=CHR$(255) &amp; CHR$(84)</td>
<td></td>
</tr>
<tr>
<td></td>
<td>OUTPUT KBD; C$; H$</td>
<td></td>
</tr>
<tr>
<td>DISP</td>
<td>PRINT</td>
<td>Display variables or literals on CRT.</td>
</tr>
<tr>
<td>ENABLE INTR 7: 8</td>
<td>ENABLE INTR 7; 2</td>
<td>Enable SRQ interrupt</td>
</tr>
<tr>
<td>STATUS 7; 1; S</td>
<td>STATUS 7; 5; S</td>
<td>Clear SRQ interrupt</td>
</tr>
<tr>
<td>DISP ’’MESSAGE’’@INPUT A$</td>
<td>INPUT ’’MESSAGE’’, A$</td>
<td>Prompt for and input variable</td>
</tr>
<tr>
<td>IF...THEN...ELSE...</td>
<td>IF...THEN...</td>
<td>Conditional branching</td>
</tr>
<tr>
<td></td>
<td>...ELSE...</td>
<td></td>
</tr>
<tr>
<td></td>
<td>END IF</td>
<td></td>
</tr>
<tr>
<td></td>
<td>ABORT 7</td>
<td>Send IFC</td>
</tr>
</tbody>
</table>

3-23
Figure 3-16. Test Configuration for Programs 1, 2, 5, and 6
Figure 3-17. Test Configuration for Programs 3 and 7
Figure 3-18. Test Configuration for Programs 4 and 8
3.8.2 Instrument Programming

The instruments in the test setups are programmed as follows:

Model 196: The DMM is programmed for autoranging in the DCV function. The instrument is programmed for the continuous trigger mode. Also the data format is configured to eliminate the prefix since only numeric information is required. Finally, the instrument is programmed to operate with 6½ digit, but with no filtering (filtering may be required in some instances, especially with low voltages).

Model 220: The current source is programmed for a voltage compliance of 10V. The current value is entered by the user to program the unit during the course of the program. For Programs 4 and 8 only, the current is reversed during the test by programming the Model 220 for a negative current of the same magnitude. Note that no provision is included for verifying that the positive and negative currents have the same magnitude. Also, averaging is used in all programs except Programs 3 and 7.

Model 485: The continuous trigger mode is used and the prefix on the data string is eliminated by appropriate programming. The unit is zero checked at the start of each program for optimum accuracy.

Model 705: The scanner is placed in the matrix mode and then crosspoint 5.4 is programmed in accordance with the resistivity configuration desired by the user. Other crosspoints are programmed as required for the various measurements.

3.8.3 Program Listings

Programs 1 through 8 listings appear on the following pages.
Program 1
Hall Voltage Measurement (HP-85 Version)

10 CLEAR
20 W=5000  ! FIVE SECOND DELAY
30 DIM A$(25),U$(8)
40 P1=712  ! 220 ADDRESS IS 12
50 P2=707  ! 196 ADDRESS IS 7
60 P3=717  ! 705 ADDRESS IS 17
70 P4=722  ! 485 ADDRESS IS 22
80 DISP "THIS PROGRAM MEASURES"
90 DISP "HALL VOLTAGES AND COMPUTES"
100 DISP "THE COEFFICIENTS."
110 DISP "THE 705S HALL CARD MUST"
120 DISP "BE IN CARD 1 LOCATION."
130 DISP
140 DISP "PRESS 'CONT'"
150 PAUSE
160 CLEAR
170 DISP "SELECT CARD RESISTIVITY SETUP"
180 DISP
190 DISP "(1) LOW RESISTIVITY"
200 DISP "(2) HIGH RESISTIVITY"
210 INPUT A
220 IF A<1 OR A>2 THEN 180
230 IF A=1 THEN Z$="N05:4X" ELSE Z$="C05:4X" ! 705 COMMAND STRING
240 DISP "INITIALIZING INSTRUMENTS"
250 REMOTE P1,P2,P3,P4 ! PUT INSTRUMENTS IN REMOTE
260 CLEAR 7 ! SEND DEVICE CLEAR
270 ! *** INITIALIZE 705 ***
280 OUTPUT P3 ;"A0X" ! MATRIX MODE
290 OUTPUT P3 ;Z$ ! PROGRAM RESISTIVITY
300 ! *** INITIALIZE 485 ***
310 OUTPUT P4 ;"R1X" ! 2NA RANGE
320 WAIT 1000 ! SETTLING TIME
330 OUTPUT P4 ;"C1X" ! ZERO CHECK ON
340 WAIT 1000 ! WAIT FOR READING TO SETTLE
350 OUTPUT P4 ;"Z1X" ! ENABLE REL
360 WAIT 1000 ! WAIT FOR REL TO COMPLETE
370 OUTPUT P4 ;"C0X" ! ZERO CHECK OFF
380 OUTPUT P4 ;"R0G1X" ! AUTORANGE,NO PREFIX
390 ! *** INITIALIZE 196 ***
400 OUTPUT P2 ;"R0F0S361X" ! AUTO,DCV,RATE,NO PREFIX
410 OUTPUT P1 ;"V10X" ! 220 10V COMPLIANCE.
420 DISP "220 CURRENT (500FA-100MA)"
430 INPUT I ! INPUT 220 CURRENT
440 I=ABS(I) ! POSITIVE CURRENT ONLY
450 IF I<5.E-13 OR I>.1 THEN 420
460 OUTPUT P1 ;"I:";I;"X" ! PROGRAM 220 CURRENT
470 DISP "ENTER SAMPLE THICKNESS(CM)"
480 INPUT T ! INPUT SAMPLE THICKNESS
490 T=ABS(T)
500 DISP "ENTER FLUX DENSITY (GAUSS)"
Program 1
Hall Voltage Measurement (HP-85 Version), Continued

510 INPUT B
520 B=ABS(B)
530 CLEAR
540 DISP "APPLY +B, PRESS 'CONT'"
550 PAUSE
560 ! ***MAIN MEASUREMENT LOOP***
570 RESTORE ! CLEAR DATA POINTER
580 DISP "MEASURING..."
590 FOR J=1 TO 8 ! LOOP FOR ALL 8 VOLTAGES
600 IF J=5 THEN RESTORE @ DISP "REVERSE FIELD (-B), PRESS 'CONT'" @ BEEP @ PAUSE
610 READ A$ ! READ 705 COMMAND STRING
620 OUTPUT P3 ;A$ ! CLOSE CROSSPOINTS
630 OUTPUT P1 ;"FX" ! TURN ON 220 OUTPUT
640 WAIT W ! WAIT W MSEC FOR READING TO SETTLE
650 ENTER P2 ; V(J) ! GET 196 VOLTAGE READING
660 IF ABS(V(J))>8 THEN GOTO 960 ! CHECK VOLTAGE LIMITS
670 IF J=1 THEN ENTER P4 ; I1 ! GET 48S CURRENT READING
680 OUTPUT P1 ;"FX" ! TURN OFF 220 OUTPUT
690 READ A$ ! READ 705 COMMAND STRING
700 OUTPUT P3 ;A$ ! OPEN CROSSPOINTS
710 NEXT J ! LOOP BACK FOR NEXT MEASUREMENT
720 BEEP
730 CLEAR
740 CLEAR 7 ! SEND DEVICE CLEAR
750 DISP "MEASUREMENTS COMPLETE"
760 DISP "MEASURED CURRENT";I1
770 FOR J=1 TO 8 ! LOOP AND DISPLAY MEASURED VOLTAGES
780 DISP "V";J;"";"";V(J)
790 NEXT J
800 DISP
810 DISP "PRESS 'CONT' TO CALCULATE"
820 DISP "HALL COEFFICIENTS"
830 PAUSE
840 R1=25000000*(V(1)+V(4)+V(5)+V(8))
850 R2=25000000*(V(2)+V(3)+V(6))
860 R3=(R1+R2)/2
870 DISP "RHC";R1
880 DISP "RHD";R2
890 DISP "RAV";R3
900 DISP
910 DISP "REPEAT TEST (Y/N)"
920 INPUT A$
930 IF A$[1,1]="Y" THEN CLEAR @ GOTO 240
940 GOTO 1090
950 ! *** ERROR CHECKING ***
960 CLEAR @ BEEP
970 CLEAR 7 ! SEND DEVICE CLEAR
980 DISP "SAMPLE VOLTAGE IS OVER"
990 DISP "8V 7055 LIMIT"
1000 DISP "DO YOU WISH TO:"
Program 1
Hall Voltage Measurement (HP-85 Version), Continued

1010 DISP "(1) RESTART MEASUREMENT"
1020 DISP "(2) ABORT THE PROGRAM"
1030 DISP
1040 INPUT A
1050 IF A<1 OR A>2 THEN 1000
1060 IF A=2 THEN 1090
1070 CLEAR
1080 GOTO 240
1090 END
1100 ! *** 705 COMMAND STRINGS ***
1110 DATA "C02:1C01:3C03:4C04:2X"
1120 DATA "N02:1N01:3N03:4N04:2X"
1130 DATA "C02:3C01:1C03:4C04:2X"
1140 DATA "N02:3N01:1N03:4N04:2X"
1150 DATA "C02:2C01:4C03:1C04:3X"
1160 DATA "N02:2N01:4N03:1N04:3X"
1170 DATA "C02:4C01:2C03:1C04:3X"
1180 DATA "N02:4N01:2N03:1N04:3X"
Program 2
Van der Pauw Resistivity (HP-85 Version)

10 CLEAR
20 W=5000 ! FIVE SECOND DELAY
30 DIM A$(25),V(8)
40 P1=712 ! 220 ADDRESS IS 12
50 P2=707 ! 196 ADDRESS IS 7
60 P3=717 ! 705 ADDRESS IS 17
70 P4=722 ! 485 ADDRESS IS 22
80 DISP "THIS PROGRAM MEASURES"
90 DISP "RESISTIVITY OF"
100 DISP "VAN DER PAUW SAMPLES"
110 DISP "THE 7065 HALL CARD MUST"
120 DISP "BE IN CARD 1 LOCATION."
130 DISP
140 DISP "PRESS 'CONT'"
150 PAUSE
160 CLEAR
170 DISP "SELECT CARD RESISTIVITY SETUP"
180 DISP
190 DISP "(1) LOW RESISTIVITY"
200 DISP "(2) HIGH RESISTIVITY"
210 INPUT A
220 IF A<1 OR A>2 THEN 180
230 IF A=1 THEN Z$="N05:4X" ELSE Z$="C05:4X" ! 705 COMMAND STRING
240 DISP "INITIALIZING INSTRUMENTS"
250 REMOTE P1,P2,P3,P4 ! PUT INSTRUMENTS IN REMOTE
260 CLEAR ? ! SEND DEVICE CLEAR
270 ! *** INITIALIZE 705 ***
280 OUTPUT P3 ;"A0X" ! MATRIX MODE
290 OUTPUT P3 ;Z$ ! PROGRAM RESISTIVITY
300 ! *** INITIALIZE 485 ***
310 OUTPUT P4 ;"R1X" ! 2NA RANGE
320 WAIT 1000 ! SETTLING TIME
330 OUTPUT P4 ;"C1X" ! ZERO CHECK ON
340 WAIT 1000
350 OUTPUT P4 ;"Z1X" ! ENABLE REL
360 WAIT 1000 ! WAIT FOR REL
370 OUTPUT P4 ;"C0X" ! ZERO CHECK OFF
390 OUTPUT P4 ;"R061X" ! AUTORANGE,NO PREFIX
390 ! *** INITIALIZE 196 ***
400 OUTPUT P2 ;"R0F0S361X" ! AUTO,DCV,RATE,NO PREFIX
410 OUTPUT P1 ;"V10X" ! 220 10V COMPLIANCE
420 DISP "220 CURRENT (500FA-100MA)"
430 INPUT I ! INPUT 220 CURRENT
440 I=ABS(I) ! POSITIVE CURRENT ONLY
450 OUTPUT P1 ;"I";I;"X" ! PROGRAM 220 CURRENT
460 DISP "ENTER SAMPLE THICKNESS(CM)"
470 INPUT T ! INPUT SAMPLE THICKNESS
480 T=ABS(T)
490 CLEAR
500 ! ***MAIN MEASUREMENT LOOP***
Program 2
Van der Pauw Resistivity (HP-85 Version), Continued

510 RESTORE ! CLEAR DATA POINTER
520 DISP "MEASURING..."
530 FOR J=1 TO 8 ! LOOP FOR ALL 8 VOLTAGES
540 READ A$ ! READ 705 COMMAND STRING
550 OUTPUT P3 ;A$ ! CLOSE CROSSPOINTS
560 OUTPUT P1 ;"FIX" ! TURN ON 220 OUTPUT
570 WAIT W ! WAIT W MSEC FOR READING TO SETTLE
580 ENTER P2 ; V(J) ! GET 196 VOLTAGE READING
590 IF ABS(V(J))>0 THEN GOTO 690 ! CHECK VOLTAGE LIMITS
600 IF J=1 THEN ENTER P4 ; I1 ! GET 485 CURRENT READING
610 OUTPUT P1 ;"FOX" ! TURN OFF 220 OUTPUT
620 READ A$ ! READ 705 COMMAND STRING
630 OUTPUT P3 ;A$ ! OPEN CROSSPOINTS
640 NEXT J ! LOOP BACK FOR NEXT MEASUREMENT
650 BEEP
660 CLEAR 7 ! SEND DEVICE CLEAR
670 DISP "MEASUREMENTS COMPLETE"
680 DISP "MEASURED CURRENT";I1
700 FOR J=1 TO 8 ! LOOP AND DISPLAY MEASURED VOLTAGES
710 DISP "V";J;"=";V(J)
720 NEXT J
730 DISP
740 DISP "PRESS 'CONT' TO CALCULATE"
750 DISP "RESISTIVITY"
760 PAUSE
770 R1=1.1331*T/I1*(V(2)+V(4)-V(1)-V(3))
780 R2=1.1331*T/I1*(V(6)+V(8)-V(5)-V(7))
790 R3=(R1+R2)/2
800 DISP "PA=";R1
810 DISP "PB=";R2
820 DISP "PHAV=";R3
830 DISP
840 DISP "REPEAT TEST (Y/N)"
850 INPUT A$
860 IF A$[1,1]="Y" THEN CLEAR @ GOTO 240
870 GOTO 1020
880 ! *** ERROR CHECKING ***
890 CLEAR @ BEEP
900 CLEAR 7 ! SEND DEVICE CLEAR
910 DISP "SAMPLE VOLTAGE IS OVER"
920 DISP "8V 7065 LIMIT"
930 DISP "DO YOU WISH TO:" 940 DISP "(1) RESTART MEASUREMENT"
950 DISP "(2) ABORT THE PROGRAM"
960 DISP
970 INPUT A
980 IF A<1 OR A>2 THEN 930
990 IF A=2 THEN 1020
1000 CLEAR
Program 2
Van der Pauw Resistivity (HP-85 Version), Continued

1010 GOTO 240
1020 END
1030 I *** 705 COMMAND STRINGS ***
1040 DATA "C01:2C02:1C03:3C04:4X"
1050 DATA "N01:2N02:1N03:3N04:4X"
1060 DATA "C02:2C01:1C03:3C04:4X"
1070 DATA "N02:2N01:1N03:3N04:4X"
1080 DATA "C02:2C01:3C03:4C04:1X"
1090 DATA "N02:2N01:3N03:4N04:1X"
1100 DATA "C02:3C01:2C03:4C04:1X"
1110 DATA "N02:3N01:2N03:4N04:1X"
1120 DATA "C02:3C01:4C03:1C04:2X"
1130 DATA "N02:3N01:4N03:1N04:2X"
1140 DATA "C02:4C01:3C03:1C04:2X"
1150 DATA "N02:4N01:3N03:1N04:2X"
1160 DATA "C02:4C01:1C03:2C04:3X"
1170 DATA "N02:4N01:1N03:2N04:3X"
1180 DATA "C02:1C01:4C03:2C04:3X"
1190 DATA "N02:1N01:4N03:2N04:3X"
Program 3
Bar Sample Resistivity (HP-85 Version)

10 CLEAR
20 W=5000 ! FIVE SECOND DELAY
30 P1=712 ! 220 ADDRESS IS 12
40 P2=707 ! 196 ADDRESS IS 7
50 P3=717 ! 705 ADDRESS IS 17
60 P4=722 ! 485 ADDRESS IS 22
70 DISP "THIS PROGRAM MEASURES"
80 DISP "RESISTIVITY OF BAR TYPE SAMPLES"
90 DISP "THE 7065 HALL CARD MUST"
100 DISP "BE IN CARD 1 LOCATION."
110 DISP
120 DISP "PRESS 'CONT'"
130 PAUSE
140 DISP "SELECT CARD RESISTIVITY SETUP"
150 DISP
160 DISP 
170 DISP "(1) LOW RESISTIVITY"
180 DISP "(2) HIGH RESISTIVITY"
190 DISP
200 INPUT A
210 IF A<1 OR A>2 THEN 150
220 IF A=1 THEN Z$="N05:4X" ELSE Z$="C05:4X" ! 705 COMMAND STRING
230 DISP "INITIALIZING INSTRUMENTS"
240 REMOTE P1,P2,P3,P4 ! PUT INSTRUMENTS IN REMOTE
250 CLEAR ? ! SEND DEVICE CLEAR
260 ! *** INITIALIZE 705 ***
270 OUTPUT P3 ;"A0X" ! MATRIX MODE
280 OUTPUT P3 ;Z$ ! PROGRAM RESISTIVITY
290 ! *** INITIALIZE 485 ***
300 OUTPUT P4 ;"R1X" ! 2NA RANGE
310 WAIT 1000 ! SETTLING TIME
320 OUTPUT P4 ;"C1X" ! ZERO CHECK ON
330 WAIT 1000
340 OUTPUT P4 ;"Z1X" ! ENABLE REL
350 WAIT 1000
360 OUTPUT P4 ;"C0X" ! ZERO CHECK OFF
370 OUTPUT P4 ;"R0G1X" ! AUTORANGE,NO PREFIX
380 ! *** INITIALIZE 196 ***
390 OUTPUT P2 ;"R0F05G1X" ! AUTO,DCU,RATE,NO PREFIX
400 OUTPUT P1 ;"V10X" ! 220 10V COMPLIANCE.
410 DISP "220 CURRENT (500FA-100MA)"
420 INPUT I ! INPUT 220 CURRENT
430 I=ABS(I)
440 IF I<5.E-13 OR I>.1 THEN 410
450 OUTPUT P1 ;"I";I;"X" ! PROGRAM 220 CURRENT
460 DISP "ENTER SAMPLE THICKNESS (CM)"
470 INPUT Z
480 Z=ABS(Z)
490 DISP "ENTER SAMPLE X DIMENSION (CM)"
500 INPUT X
Program 3
Bar Sample Resistivity (HP-85 Version), Continued

510 X=ABS(X)
520 DISP "ENTER SAMPLE Y DIMENSION (CM)"
530 INPUT Y
540 Y=ABS(Y)
550 CLEAR
560 ! *** MAKE MEASUREMENTS ***
570 OUTPUT P3 ;"Y02:4C03:1C04:2X" ! CLOSE CROSSPOINTS
580 OUTPUT P1 ;"F1X" ! TURN ON 220 OUTPUT
590 WAIT W ! WAIT FOR READING TO SETTLE
600 ENTER P2 ; V ! GET VOLTAGE READING FROM 195
610 IF ABS(V)<8 THEN 820 ! CHECK VOLTAGE LIMITS
620 ENTER P4 ; I! ! GET CURRENT READING FROM 485
630 OUTPUT P1 ;"F0X" ! TURN OFF 220 OUTPUT
640 OUTPUT P3 ;"N02:4N03:1N04:2X" ! OPEN CROSSPOINTS
650 BEEP
660 CLEAR
670 CLEAR 7 ! SEND DEVICE CLEAR
680 DISP "MEASUREMENTS COMPLETE"
690 DISP "MEASURD CURRENT";I!!
700 DISP "MEASURED VOLTAGE";V
710 DISP "PRESS 'CONT' TO CALCULATE"
720 DISP "RESISTIVITY"
730 PAUSE
740 P=V*Y/X/(X*I)
750 Disp "RESISTIVITY=";P;"OHM-CM"
760 Disp
770 Disp "REPEAT TEST (Y/N)"
780 Input A$
790 IF A$[1,1]="Y" THEN CLEAR @ GOTO 230
800 GOTO 950
810 ! *** ERROR CHECKING ***
820 CLEAR @ BEEP
830 CLEAR 7 ! SEND DEVICE CLEAR
840 DISP "SAMPLE VOLTAGE IS OVER"
850 DISP "8V 7065 LIMIT"
860 DISP "DO YOU WISH TO:"
870 DISP "(1) RESTART MEASUREMENT"
880 DISP "(2) ABORT THE PROGRAM"
890 DISP
900 INPUT A
910 IF A<1 OR A>2 THEN 860
920 IF A=2 THEN 950
930 CLEAR
940 GOTO 230
950 END
Program 4
6- and 8-Contact Sample Resistivity (HP-85 Version)

10 CLEAR
20 W=5000 ! FIVE SECOND DELAY
30 DIM A$(25),V(4)
40 P1=712 ! 220 ADDRESS IS 12
50 P2=707 ! 196 ADDRESS IS 7
60 P3=717 ! 705 ADDRESS IS 17
70 P4=722 ! 485 ADDRESS IS 22
80 DISP "THIS PROGRAM MEASURES"
90 DISP "PARAMETERS TO CALCULATE"
100 DISP "RESISTIVITIES OF 6 OR"
110 DISP "8 CONTACT SAMPLES"
120 DISP "THE 7065 HALL CARD MUST"
130 DISP "BE IN CARD 1 LOCATION."
140 DISP
150 DISP "PRESS 'CONT'"
150 PAUSE
170 CLEAR
180 DISP "SELECT CARD RESISTIVITY SETUP"
190 DISP
200 DISP "(1) LOW RESISTIVITY"
210 DISP "(2) HIGH RESISTIVITY"
220 INPUT A
230 IF A<1 OR A>2 THEN 190
240 IF A=1 THEN Z$="N05:4X" ELSE Z$="C05:4X" ! 705 COMMAND STRING
250 DISP
260 DISP "INITIALIZING INSTRUMENTS"
270 REMOTE P1,P2,P3,P4 ! PUT INSTRUMENTS IN REMOTE
280 CLEAR 7 ! SEND DEVICE CLEAR
290 ! *** INITIALIZE 705 ***
300 OUTPUT P3 ;"A0X" ! MATRIX MODE
310 OUTPUT P3 ;Z$ ! PROGRAM RESISTIVITY
320 ! *** INITIALIZE 485 ***
330 OUTPUT P4 ;"R1X" ! 2NA RANGE
340 WAIT 1000 ! SETTLING TIME
350 OUTPUT P4 ;"C1X" ! ZERO CHECK ON
360 WAIT 1000
370 OUTPUT P4 ;"Z1X" ! ENABLE REL
380 WAIT 1000
390 OUTPUT P4 ;"C0X" ! ZERO CHECK OFF
400 OUTPUT P4 ;"R0G1X" ! AUTORANGE,NO PREFIX
410 ! *** INITIALIZE 196 ***
420 OUTPUT P2 ;"R0F0361X" ! AUTORANGE,DCV,RATE,NO PREFIX
430 OUTPUT P1 ;"V10X" ! 220 10V COMPLIANCE
440 DISP "220 CURRENT (500FA-100MA)"
450 INPUT I ! INPUT 220 CURRENT
460 I=ABS(I)
470 IF I<5.E-13 OR I>.1 THEN 440
480 I2=-I ! DEFINE NEGATIVE CURRENT OF SAME MAGNITUDE
490 OUTPUT P4 ;"I1X" ! PROGRAM 220 CURRENT
500 DISP "ENTER SAMPLE THICKNESS(CM)"
Program 4
6- and 8-Contact Sample Resistivity (HP-85 Version), Continued

510 INPUT T
520 T=ABS(T)
530 DISP "ENTER SAMPLE WIDTH (CM)"
540 INPUT W1
550 W1=ABS(W1)
560 DISP "ENTER SAMPLE D1 DIMENSION (CM)"
570 INPUT D1
580 D1=ABS(D1)
590 DISP "ENTER SAMPLE D2 DIMENSION (CM)"
600 INPUT D2
610 D2=ABS(D2)
620 DISP "PRESS 'CONT' TO MEASURE"
630 PAUSE
640 ! ***MAIN MEASUREMENT LOOP***
650 RESTORE ! CLEAR DATA POINTER
660 DISP "MEASURING..."
670 FOR J=1 TO 4 ! LOOP FOR ALL FOUR VOLTAGES
680 IF J=3 THEN RESTORE @ OUTPUT P1 ;"I";I2;"X" ! REVERSE CURRENT
690 READ A$ ! READ 705 COMMAND STRING
700 OUTPUT P3 ;A$ ! CLOSE CROSSPOINTS
710 OUTPUT P1 ;"F1X" ! TURN ON 220 OUTPUT
720 WAIT W ! WAIT FOR READINGS TO SETTLE
730 ENTER P2 ; V(J) ! GET 196 READING
740 IF ABS(V(J))>8 THEN GOTO 1030 ! CHECK READING LIMITS
750 IF J=1 THEN ENTER P4 ; I1 ! INPUT 48S CURRENT READING
760 OUTPUT P1 ;"F0X" ! TURN OFF 220 OUTPUT
770 READ A$ ! READ 705 COMMAND STRING
780 OUTPUT P3 ;A$ ! OPEN CROSSPOINTS
790 NEXT J ! LOOP BACK FOR NEXT MEASUREMENT
800 BEEP
810 CLEAR
820 CLEAR 7 ! SEND DEVICE CLEAR
830 DISP "MEASUREMENTS COMPLETE"
840 DISP "MEASURED CURRENT";I1
850 FOR J=1 TO 4 ! DISPLAY MEASURED VOLTAGES
860 DISP "V";J;"=";V(J)
870 NEXT J
880 DISP
890 DISP "PRESS 'CONT' TO CALCULATE"
900 DISP "RESISTIVITIES"
910 PAUSE
920 P1=W1*T/(2*I1*D1)*(V(1)-V(3))
930 P2=W1*T/(2*I1*D2)*(V(2)-V(4))
940 P3=(P1+P2)/2
950 DISP "pA";P1
960 DISP "pB";P2
970 DISP "pAVG";P3
980 DISP "REPEAT TEST (Y/N)"
990 INPUT A$
1000 IF A$[1,1]="Y" THEN CLEAR @ GOTO 250
Program 4
6- and 8-Contact Sample Resistivity (HP-85 Version), Continued

1010 GOTO 1160
1020 I *** ERROR CHECKING ***
1030 CLEAR @ BEEP
1040 CLEAR 7 I SEND DEVICE CLEAR
1050 DISP "SAMPLE VOLTAGE IS OVER"
1060 DISP "8V 7065 LIMIT"
1070 DISP "DO YOU WISH TO:"
1080 DISP "(1) RESTART MEASUREMENT"
1090 DISP "(2) ABORT THE PROGRAM"
1100 DISP
1110 INPUT A
1120 IF A<1 OR A>2 THEN 1070
1130 IF A=2 THEN 1160
1140 CLEAR
1150 GOTO 260
1160 END
1170 I *** 705 COMMAND STRINGS ***
1180 DATA "C03:2C04:1X"
1190 DATA "N03:2N04:1X"
1200 DATA "C03:3C04:4X"
1210 DATA "N03:3N04:4X"
Program 5
Hall Voltage Measurement (IBM-PC/8573A Version)

10 CLS
20 DELAY=5000 'FIVE SECOND DELAY
30 DIM V(8)
40 D$= CHR$(20) 'DEFINE DCL COMMAND BYTE
50 P1%=12: P2%=7: P3%=17: P4%=22 'DEFINE INSTRUMENT PRIMARY ADDRESSES
60 NA$="GPIBO":CALL IBFIND(NA$,BRD0)$ "FIND BOARD DESCRIPTOR
70 NA$="DEV1":CALL IBFIND(NA$,M220)$ 'FIND 220 DESCRIPTOR
80 NA$="DEV2":CALL IBFIND(NA$,M196)$ 'FIND 195 DESCRIPTOR
90 NA$="DEV3":CALL IBFIND(NA$,M705)$ 'FIND 705 DESCRIPTOR
100 NA$="DEV4":CALL IBFIND(NA$,M485)$ 'FIND 485 DESCRIPTOR
110 CALL IBBAD(M220$,P1%) 'SET 220 PRIMARY ADDRESS
120 CALL IBBAD(M196$,P2%) 'SET 195 PRIMARY ADDRESS
130 CALL IBBAD(M705$,P3%) 'SET 705 PRIMARY ADDRESS
140 CALL IBBAD(M485$,P4%) 'SET 485 PRIMARY ADDRESS
150 PRINT"THIS PROGRAM MEASURES HALL VOLTAGES AND COMPUTES THE"
160 PRINT"COEFFICIENTS. THE HALL CARD MUST BE IN THE CARD 1 LOCATION"
170 PRINT
180 PRINT"PRESS ANY KEY TO CONTINUE"
190 A$=INKEY$:IF A$= "" THEN 190
200 CLS
210 PRINT"SELECT CARD RESISTIVITY SETUP"
220 PRINT
230 PRINT"(1) LOW RESISTIVITY"
240 PRINT"(2) HIGH RESISTIVITY"
250 INPUT A
260 IF A<1 OR A>2 THEN 210
270 IF A=1 THEN Z$="N05:4X" ELSE Z$="C05:4X" '70S COMMAND STRING
280 PRINT"INITIALIZING INSTRUMENTS"
290 V%=1:CALL IBSRE(BRD0$,V%) 'SEND REMOTE ENABLE
300 CALL IBCMD(BRD0$,D$) 'SEND DEVICE CLEAR
310 REM ***** INITIALIZE THE MODEL 70S ********
320 C$="A0X":CALL IBWRT(M705$,C$) 'PUT 705 IN MATRIX MODE
330 CALL IBWRT(M705$,Z$) 'PROGRAM CARD RESISTIVITY SETUP
340 REM ***** INITIALIZE THE 485 ********
350 C$="R1X":CALL IBWRT(M485$,C$) 'SELECT 2NA RANGE
360 FOR I = 1 TO 1000:NEXT I 'SETTLING TIME
370 C$="C1X":CALL IBWRT(M485$,C$) 'ZERO CHECK ON
380 FOR I = 1 TO 1000:NEXT I
390 C$="Z1X":CALL IBWRT(M485$,C$) 'REL ON
400 FOR I = 1 TO 1000:NEXT I
410 C$="C0X":CALL IBWRT(M485$,C$) 'ZERO CHECK OFF
420 C$="R0G1X":CALL IBWRT(M485$,C$) 'AUTORANGE,NO PREFIX
430 REM ***** INITIALIZE THE 195 *******
440 C$="RF0S3G1X":CALL IBWRT(M196$,C$) 'AUTO,DCV,RATE,NO PREFIX
450 REM ***** INITIALIZE THE 220 *******
460 C$="V10X":CALL IBWRT(M220$,C$) '220 10V COMPLIANCE
470 INPUT"220 CURRENT (500FA-100MA)";I 'INPUT 220 CURRENT
480 I=ABS(I) 'POSITIVE CURRENT ONLY
490 IF I< 5E-13 OR I> 1.1 THEN 470
500 C$="I"+STR$(I)+"X":CALL IBWRT(M220$,C$) 'PROGRAM 220 CURRENT
Program 5
Hall Voltage Measurement (IBM-PC/8573A Version), Continued

510 INPUT "ENTER SAMPLE THICKNESS (CM)";T
520 T=ABS(T)
530 INPUT "ENTER FLUX DENSITY (GAUSS)";B
540 B=ABS(B)
550 CLS
560 PRINT "APPLY +B, PRESS ANY KEY TO CONTINUE"
570 A$=INKEY$:IF A$="" THEN 570
580 REM ****** MAIN MEASUREMENT LOOP *******
590 RESTORE
600 CLS
610 PRINT "MEASURING..."
620 FOR J=1 TO 8
630 IF J>5 THEN 660
640 RESTORE:PRINT "REVERSE FIELD (-B), PRESS ANY KEY TO CONTINUE";BEEP
650 A$=INKEY$:IF A$="" THEN 550
660 READ C$:CALL IBWRT(M705%,C$) 'CLOSE CROSSPOINTS
670 C$="F1X";CALL IBWRT(M220%,C$) 'TURN ON 220 OUTPUT
680 FOR I=1 TO DELAY:NEXT 'WAIT FOR READING TO SETTLE
690 RD$=SPACE$(25):CALL IBRD(M196%,RD$) 'GET 196 READING
700 V(J)=VAL(RD$) 'CONVET STRING TO NUMERIC, PUT IN ARRAY
710 IF ABS(V(J))>8 THEN 990 'CHECK VOLTAGE LIMITS
720 IF J<6 THEN 750
730 RD$=SPACE$(25):CALL IBRD(M485%,RD$) 'GET 485 CURRENT READING
740 I$=VAL(RD$) 'CONVET READING TO NUMERIC
750 C$="F1X";CALL IBWRT(M220%,C$) 'TURN OFF 220 OUTPUT
760 READ C$ 'READ 705 COMMAND STRING
770 CALL IBWRT(M705%,C$) 'OPEN CROSSPOINTS
780 NEXT J 'LOOP BACK FOR NEXT POINT
790 BEEP
800 CALL IBCM(00%,D$) 'SEND DEVICE CLEAR
810 CLS
820 PRINT "MEASUREMENTS COMPLETE."
830 PRINT "MEASURED CURRENT";I$
840 FOR J=1 TO 8 'LOOP AND DISPLAY MEASURED VOLTAGES
850 PRINT "V";J;"=";V(J)
860 NEXT J
870 PRINT "PRESS ANY KEY TO CALCULATE HALL COEFFICIENTS"
880 A$=INKEY$:IF A$="" THEN 880
890 RHC=2.5E+07*(I$+I$)*(V(2)-V(1)+V(5)-V(6))
900 RHD=2.5E+07*(I$+I$)*(V(4)-V(3)+V(7)-V(8))
910 RHAVG=(RHC+RHD)/2
920 PRINT "RHC";RHC
930 PRINT "RHD";RHD
940 PRINT "RAVAG";RAVAG
950 PRINT
960 INPUT "REPEAT TEST (Y/N)";A$
970 IF LEFTS(A$)="Y" THEN CLS:GOTO 280
980 GOTO 1100
990 CLS:BEEP
1000 CALL IBCM(00%,D$) 'SEND DEVICE CLEAR

3-40
Program 5
Hall Voltage Measurement (IBM-PC/8573A Version), Continued

1010 PRINT"SAMPLE VOLTAGE IS OVER 8V 7065 LIMIT"
1020 PRINT"DO YOU WISH TO:"
1030 PRINT"(1) RESTART THE MEASUREMENT"
1040 PRINT"(2) ABORT THE PROGRAM"
1050 PRINT
1060 INPUT A
1070 IF A<1 OR A>2 THEN 1020
1080 IF A=2 THEN 1100
1090 CLS:GOTO 200
1100 END 'END PROGRAM
1110 REM ******* 705 COMMAND STRINGS *********
1120 DATA "C02:1C01:3C03:4C04:2X"
1130 DATA "N02:1N01:3N03:4N04:2X"
1140 DATA "C02:3C01:1C03:4C04:2X"
1150 DATA "N02:3N01:1N03:4N04:2X"
1160 DATA "C02:2C01:4C03:1C04:3X"
1170 DATA "N02:2N01:4N03:1N04:3X"
1180 DATA "C02:4C01:2C03:1C04:3X"
1190 DATA "N02:4N01:2N03:1N04:3X"
Program 6
Van der Pauw Resistivity (IBM-PC/8573A Version)

10 CLS
20 DELAY=5000 'FIVE SECOND DELAY
30 DIM V(8)
40 D$=CHR$(20) ' DEFINE DCL BYTE
50 P1%=12;P2%=7;P3%=17;P4%=22 'DEFINE INSTRUMENT PRIMARY ADDRESSES
60 NA$="GPIB0":CALL IBFIND(NA$,BRD0%) 'FIND BOARD DESCRIPTOR
70 NA$="DEV1":CALL IBFIND(NA$,M220%) 'FIND 220 DESCRIPTOR
80 NA$="DEV2":CALL IBFIND(NA$,M196%) 'FIND 196 DESCRIPTOR
90 NA$="DEV3":CALL IBFIND(NA$,M705%) 'FIND 705 DESCRIPTOR
100 NA$="DEV4":CALL IBFIND(NA$,M485%) 'FIND 485 DESCRIPTOR
110 CALL IBPAD(M220%,P1%) 'SET 220 PRIMARY ADDRESS
120 CALL IBPAD(M196%,P2%) 'SET 196 PRIMARY ADDRESS
130 CALL IBPAD(M705%,P3%) 'SET 705 PRIMARY ADDRESS
140 CALL IBPAD(M485%,P4%) 'SET 485 PRIMARY ADDRESS
150 PRINT"THIS PROGRAM MEASURES THE RESISTIVITY OF VAN DER PAUW SAMPLES"
160 PRINT"THE HALL CARD MUST BE IN THE CARD 1 LOCATION"
170 PRINT
180 PRINT"PRESS ANY KEY TO CONTINUE"
190 A$=INKEY$:IF A$="" THEN 190
200 CLS
210 PRINT"SELECT CARD RESISTIVITY SETUP"
220 PRINT
230 PRINT"(1) LOW RESISTIVITY"
240 PRINT"(2) HIGH RESISTIVITY"
250 INPUT A
260 IF A<1 OR A>2 THEN 210
270 IF A=1 THEN Z$="N05:4X" ELSE Z$="C05:4X" '705 COMMAND STRING
280 PRINT"INITIALIZING INSTRUMENTS"
290 V$=1:CALL IBSET(BRD0%,V$) 'SEND REMOTE ENABLE
300 CALL IBCMD(BRD0%,D$) 'SEND DEVICE CLEAR
310 REM '******** INITIALIZING THE MODEL 705 ********
320 C$="A0X":CALL IBWR(M705%,C$) 'PUT 705 IN MATRIX MODE
330 CALL IBWR(M705%,Z$) 'PROGRAM CARD RESISTIVITY SETUP
340 REM '******** INITIALIZING THE 485 ********
350 C$="R1X":CALL IBWR(M485%,C$) 'SELECT 2NA RANGE
360 FOR I = 1 TO 1000:NEXT I 'SETTLING TIME
370 C$="C1X":CALL IBWR(M485%,C$) 'ZERO CHECK ON
380 FOR I = 1 TO 1000:NEXT I
390 C$="Z1X":CALL IBWR(M485%,C$) 'REL ON
400 FOR I = 1 TO 1000:NEXT I
410 C$="C0X":CALL IBWR(M485%,C$) 'ZERO CHECK OFF
420 C$="R0G1X":CALL IBWR(M485%,C$) 'AUTORANGE,NO PREFIX
430 REM '******** INITIALIZING THE 196 ********
440 C$="R0F05361X":CALL IBWR(M196%,C$) 'AUTO,DCV,RATE,NO PREFIX
450 REM '******** INITIALIZING THE 220 ********
460 C$="V10X":CALL IBWR(M220%,C$) '220 10V COMPLIANCE
470 INPUT"220 CURRENT (500FA-100MA)";I 'INPUT 220 CURRENT
480 I=ABS(I) 'POSITIVE CURRENT ONLY
490 IF I<5E-13 OR I>.1 THEN 470
500 C$="I+:STR$(I):+"X":CALL IBWR(M220%,C$) 'PROGRAM 220 CURRENT
Program 6
Van der Pauw Resistivity (IBM-PC/8573A Version), Continued

510 INPUT "ENTER SAMPLE THICKNESS (CM)"; T
520 T=ABS(T)
530 CLS
540 REM ***** MAIN MEASUREMENT LOOP ************
550 RESTORE
560 CLS
570 PRINT "MEASURING..."
580 FOR J=1 TO 8
590 READ C$: CALL IBWR(M705%, C$) 'CLOSE CROSSPOINTS
600 C$="FX"; CALL IBWR(M220%, C$) ' TURN ON 220 OUTPUT
610 FOR I=1 TO DELAY:NEXT 'WAIT FOR READING TO SETTLE
620 RD$=SPACE$(25): CALL IBRD(M196%, RD$) ' GET 196 READING
630 V(J)= VAL(RD$) ' CONVERT STRING TO NUMERIC, PUT IN ARRAY
640 IF ABS(V(J))<8 THEN 920 ' CHECK VOLTAGE LIMITS
650 IF J<>1 THEN 680
660 RD$=SPACE$(25): CALL IBRD(M485%, RD$) ' GET 405 CURRENT READING
670 I=VAL(RD$) ' CONVERT READING TO NUMERIC
680 C$="FX"; CALL IBWR(M220%, C$) ' TURN OFF 220 OUTPUT
690 READ C$ ' READ 705 COMMAND STRING
700 CALL IBWR(M705%, C$) ' OPEN CROSSPOINTS
710 NEXT J ' LOOP BACK FOR NEXT POINT
720 BEEP
730 CLS
740 CALL IBCMD(BRD0%, D$) ' SEND DEVICE CLEAR
750 PRINT "MEASUREMENTS COMPLETE."
760 PRINT "MEASURED CURRENT":I
770 FOR J=1 TO 8 ' LOOP AND DISPLAY MEASURED VOLTAGES
780 PRINT "V"; J; ":"; V(J)
790 NEXT J
800 PRINT "PRESS ANY KEY TO CALCULATE RESISTIVITY OF SAMPLE"
810 A$=INKEY$: IF A$="" THEN 810
820 PA=1.131*T/I1*(V(2)+V(4)-V(1)-V(3))
830 PB=1.131*T/I1*(V(6)+V(8)-V(5)-V(7))
840 PAVG=(PA+PB)/2
850 PRINT "PA="; PA
850 PRINT "PB="; PB
870 PRINT "PAVG="; PAVG
880 PRINT
890 INPUT "REPEAT TEST (Y/N)"; A$
900 IF LEFT$(A$, 1) = "Y" THEN CLS: GOTO 200
910 GOTO 1030
920 CLS: BEEP
930 CALL IBCMD(BRD0%, D$) ' SEND DEVICE CLEAR
940 PRINT "SAMPLE VOLTAGE IS OVER 8V 706S LIMIT"
950 PRINT "DO YOU WISH TO:"
960 PRINT "(1) RESTART THE MEASUREMENT"
970 PRINT "(2) ABORT THE PROGRAM"
980 PRINT
990 INPUT A
1000 IF A<1 OR A>2 THEN 950
Program 6
Van der Pauw Resistivity (IBM-PC/8573A Version), Continued

1010 IF A=2 THEN 1030
1020 CLS: GOTO 200
1030 END 'END PROGRAM
1040 REM ******* 70S COMMAND STRINGS *******
1050 DATA "C01:2C02:1C03:3C04:4X"
1060 DATA "N01:2N02:1N03:3N04:4X"
1070 DATA "C02:2C01:1C03:3C04:4X"
1080 DATA "N02:2N01:1N03:3N04:4X"
1090 DATA "C02:2C01:3C03:4C04:1X"
1100 DATA "N02:2N01:3N03:4N04:1X"
1110 DATA "C02:3C01:2C03:4C04:1X"
1120 DATA "N02:3N01:2N03:4N04:1X"
1130 DATA "C02:3C01:4C03:1C04:2X"
1140 DATA "N02:3N01:4N03:1N04:2X"
1150 DATA "C02:4C01:3C03:1C04:2X"
1150 DATA "N02:4N01:3N03:1N04:2X"
1170 DATA "C02:4C01:1C03:2C04:3X"
1180 DATA "N02:4N01:1N03:2N04:3X"
1190 DATA "C02:1C01:4C03:2C04:3X"
1200 DATA "N02:1N01:4N03:2N04:3X"
Program 7
Bar Sample Resistivity (IBM-PC/8573A Version)

10  CLS
20  DELAY=5000 'FIVE SECOND DELAY
30  P1%=12;P2%=7;P3%=17;P4%=22 'DEFINE INSTRUMENT PRIMARY ADDRESSES
40  DB=CHR$(20) ' DEFINE DCL BYTE
50  NA$="6PIB0";CALL IBFIND(NA$,,BRD0%); 'FIND BOARD DESCRIPTOR
60  NA$="DEV1";CALL IBFIND(NA$,,M220%); 'FIND 220 DESCRIPTOR
70  NA$="DEV2";CALL IBFIND(NA$,,M196%); 'FIND 196 DESCRIPTOR
80  NA$="DEV3";CALL IBFIND(NA$,,M705%); 'FIND 705 DESCRIPTOR
90  NA$="DEV4";CALL IBFIND(NA$,,M485%); 'FIND 485 DESCRIPTOR
100 CALL IBPAD(M220%,P1%) 'SET 220 PRIMARY ADDRESS
110 CALL IBPAD(M196%,P2%) 'SET 196 PRIMARY ADDRESS
120 CALL IBPAD(M705%,P3%) 'SET 705 PRIMARY ADDRESS
130 CALL IBPAD(M485%,P4%) 'SET 485 PRIMARY ADDRESS
140 PRINT"THIS PROGRAM MEASURES THE RESISTIVITY OF BAR TYPE SAMPLES"
150 PRINT"THE HALL CARD MUST BE IN THE CARD 1 LOCATION"
160 PRINT
170 PRINT"PRESS ANY KEY TO CONTINUE"
180 A$=INKEY$;IF A$="" THEN 180
190 CLS
200 PRINT"SELECT CARD RESISTIVITY SETUP"
210 PRINT
220 PRINT"(1) LOW RESISTIVITY"
230 PRINT"(2) HIGH RESISTIVITY"
240 INPUT A
250 IF A<1 OR A>2 THEN 200
260 IF A=1 THEN Z$="N05:4X" ELSE Z$="C05:4X" '705 COMMAND STRING
270 PRINT"INITIALIZING INSTRUMENTS"
280 V%=1;CALL IBSTRE(BRD0%,V%) 'SEND REMOTE ENABLE
290 CALL IBCMD(BRD0%,DB) 'SEND DEVICE CLEAR
300 REM ***** INITIALIZE THE MODEL 705 ********
310 C$="A0X";CALL IBWRT(M705%,C$) 'PUT 705 IN MATRIX MODE
320 CALL IBWRT(M705%,Z$) 'PROGRAM CARD RESISTIVITY SETUP
330 REM ***** INITIALIZE THE 485 ********
340 C$="R1X";CALL IBWRT(M485%,C$) 'SELECT 2NA RANGE
350 FOR I = 1 TO 1000:NEXT I 'SETTLING TIME
360 C$="C1X";CALL IBWRT(M485%,C$) 'ZERO CHECK ON
370 FOR I = 1 TO 1000:NEXT I
380 C$="Z1X";CALL IBWRT(M485%,C$) 'REL ON
390 FOR I = 1 TO 1000:NEXT I
400 C$="C0X";CALL IBWRT(M485%,C$) 'ZERO CHECK OFF
410 C$="R061X";CALL IBWRT(M485%,C$) 'AUTORANGE,NO PREFIX
420 REM ***** INITIALIZE THE 196 ********
430 C$="R0F03261X";CALL IBWRT(M196%,C$) 'AUTO,DCV,RATE,NO PREFIX
440 REM ***** INITIALIZE THE 220 ********
450 C$="V10X";CALL IBWRT(M220%,C$) '220 10V COMPLIANCE
460 INPUT"220 CURRENT (500FA-100MA)";I 'INPUT 220 CURRENT
470 I=ABS(I) 'POSITIVE CURRENT ONLY
480 IF I<5E-13 OR I>I THEN 460
490 C$="I1+STR$(I)+"X";CALL IBWRT(M220%,C$) 'PROGRAM 220 CURRENT
500 INPUT"ENTER SAMPLE THICKNESS (CM)";Z
Program 7
Bar Sample Resistivity (IBM-PC/8573A Version), Continued

510 Z=ABS(Z)
520 INPUT"ENTER SAMPLE X DIMENSION (CM)";X
530 X=ABS(X)
540 INPUT"ENTER SAMPLE Y DIMENSION (CM)";Y
550 Y=ABS(Y)
560 REM ******* MAKE MEASUREMENTS *********
570 CLS
580 PRINT"MEASURING..."
590 C$="C02:4C03:1C04:2X":CALL IBWRT(M705%,C$) 'CLOSE CROSSPOINTS
600 C$="F1X":CALL IBWRT(M220%,C$) ' TURN ON 220 OUTPUT
610 FOR I=1 TO DELAY:NEXT 'WAIT FOR READING TO SETTLE
620 RD$=SPACE$(25):CALL IBRD(M196%,RD$) ' GET 196 READING
630 V=VAL(RD$) ' CONVERT STRING TO NUMERIC
640 IF ABS(V)>8 THEN 830 ' CHECK VOLTAGE LIMITS
650 RD$=SPACE$(25):CALL IBRD(M485%,RD$) ' GET 485 CURRENT READING
660 I1=VAL(RD$) ' CONVERT READING TO NUMERIC
670 C$="F0X":CALL IBWRT(M220%,C$) ' TURN OFF 220 OUTPUT
680 C$="N02:4N03:1N04:2X":CALL IBWRT(M705%,C$) ' OPEN CROSSPOINTS
690 BEEP
700 CLS
710 CALL IBCMD(BRD0%,D$) ' SEND DEVICE CLEAR
720 PRINT"MEASUREMENTS COMPLETE."
730 PRINT"MEASURED CURRENT";I1
740 PRINT"MEASURED VOLTAGE";V
750 PRINT"PRESS ANY KEY TO CALCULATE RESISTIVITY"
760 A$=INKEY$:IF A$="" THEN 760
770 P=V*Y*X/(X*I1) 'CALCULATE RESISTIVITY
780 PRINT"RESISTIVITY=";P:"OHM-CM"
790 PRINT
800 INPUT"REPEAT TEST (Y/N)";A$
810 IF LEFT$(A$,1)="Y" THEN CLS:GOTO 270
820 GOTO 940
830 CLS:BEEP
840 CALL IBCMD(BRD0%,D$) ' SEND DEVICE CLEAR
850 PRINT"SAMPLE VOLTAGE IS OVER 8V 7055 LIMIT"
860 PRINT"DO YOU WISH TO:"
870 PRINT"(1) RESTART THE MEASUREMENT"
880 PRINT"(2) ABORT THE PROGRAM"
890 PRINT
900 INPUT A
910 IF A<1 OR A>2 THEN 860
920 IF A=2 THEN 940
930 CLS:GOTO 270
940 END 'END PROGRAM
Program 8
6- and 8-Contact Sample Resistivity (IBM-PC/8573A Version)

10 CLS
20 DELAY=5000 'FIVE SECOND DELAY
30 DIM V(4)
40 D$=CHR$(20) 'DEFINE DCL COMMAND BYTE
50 P1%=12:P2%=7:P3%=17:P4%=22 'DEFINE INSTRUMENT PRIMARY ADDRESSES
60 NA$="GPIB0":CALL IBFIND(NA$,BRD0%) 'FIND BOARD DESCRIPTOR
70 NA$="DEV1":CALL IBFIND(NA$,M220%) 'FIND 220 DESCRIPTOR
80 NA$="DEV2":CALL IBFIND(NA$,M195%) 'FIND 195 DESCRIPTOR
90 NA$="DEV3":CALL IBFIND(NA$,M705%) 'FIND 705 DESCRIPTOR
100 NA$="DEV4":CALL IBFIND(NA$,M485%) 'FIND 485 DESCRIPTOR
110 CALL IBPAD(M220%,P1%) 'SET 220 PRIMARY ADDRESS
120 CALL IBPAD(M195%,P2%) 'SET 195 PRIMARY ADDRESS
130 CALL IBPAD(M705%,P3%) 'SET 705 PRIMARY ADDRESS
140 CALL IBPAD(M485%,P4%) 'SET 485 PRIMARY ADDRESS
150 PRINT"THIS PROGRAM MEASURES PARAMETERS TO CALCULATE THE RESISTIVITY"
160 PRINT"OF 6- OR 8-CONTACT SAMPLES."
170 PRINT"THE HALL CARD MUST BE IN THE CARD 1 LOCATION"
180 PRINT
190 PRINT"PRESS ANY KEY TO CONTINUE"
200 A$=INKEY$:IF A$=" " THEN 200
210 CLS
220 PRINT"SELECT CARD RESISTIVITY SETUP"
230 PRINT
240 PRINT"(1) LOW RESISTIVITY"
250 PRINT"(2) HIGH RESISTIVITY"
260 INPUT A
270 IF A<1 OR A>2 THEN 220
280 IF A=1 THEN Z$="NO5:4X" ELSE Z$="CO5:4X" '705 COMMAND STRING
290 PRINT"INITIALIZING INSTRUMENTS"
300 VX$=1:CALL IBREG(BRD0%,VX$) 'SEND REMOTE ENABLE
310 CALL IBCMD(BRD0%,D$) 'SEND DEVICE CLEAR
320 REM ***** INITIALIZE THE MODEL 705 ********
330 CS$="A0X":CALL IBWRT(M705%,CS$) 'PUT 705 IN MATRIX MODE
340 CALL IBWRT(M705%,Z$) 'PROGRAM CARD RESISTIVITY SETUP
350 REM ***** INITIALIZE THE 485 ********
360 CS$="R1X":CALL IBWRT(M485%,CS$) 'SELECT 2NA RANGE
370 FOR I = 1 TO 1000:NEXT I 'SETTLING TIME
380 CS$="C1X":CALL IBWRT(M485%,CS$) 'ZERO CHECK ON
390 FOR I = 1 TO 1000:NEXT I
400 CS$="Z1X":CALL IBWRT(M485%,CS$) 'REL ON
410 FOR I = 1 TO 1000:NEXT I
420 CS$="C0X":CALL IBWRT(M485%,CS$) 'ZERO CHECK OFF
430 CS$="R051X":CALL IBWRT(M485%,CS$) 'AUTORANGE,NO PREFIX
440 REM ***** INITIALIZE THE 195 ********
450 CS$="R0F05361X":CALL IBWRT(M195%,CS$) 'AUTO,DCV,RATE,NO PREFIX
460 REM ***** INITIALIZE THE 220 ******
470 CS$="VI0X":CALL IBWRT(M220%,CS$) '220 10V COMPLIANCE
480 INPUT"220 CURRENT (500FA-100MA)";I 'INPUT 220 CURRENT
490 I=ABS(I)
500 IF I<5E-13 OR I>1.1 THEN 480

3-47
Program 8
6- and 8-Contact Sample Resistivity (IBM-PC/8573A Version), Continued

510 I2=-I
520 CS="I"+STR$(I)+"X":CALL IBWRT(M220%,CS$) 'PROGRAM 220 CURRENT
530 INPUT"ENTER SAMPLE THICKNESS (CM)";T
540 T=ABS(T)
550 INPUT"SAMPLE WIDTH (CM)";W
560 W=ABS(W)
570 INPUT"SAMPLE D1 DIMENSION (CM)";D1
580 INPUT"SAMPLE D2 DIMENSION (CM)";D2
590 D1=ABS(D1);D2=ABS(D2)
600 CLS
610 PRINT"PRESS ANY KEY TO MEASURE"
620 A$=INKEY$:IF A$="" THEN 520
630 REM ***** MAIN MEASUREMENT LOOP *******
640 RESTORE
650 CLS
660 PRINT"MEASURING..."
670 FOR J=1 TO 4
680 IF J>3 THEN 710
690 RESTORE 'CLEAR DATA POINTER
700 CS="I"+STR$(I)+"X":CALL IBWRT(M220%,CS$) 'REVERSE CURRENT
710 READ C$:CALL IBWRT(M705%,C$) 'CLOSE CROSSPOINTS
720 CS="F1X":CALL IBWRT(M220%,CS$) 'TURN ON 220 OUTPUT
730 FOR I=1 TO DELAY 'WAIT FOR READING TO SETTLE
740 RD$=SPACE$(25):CALL IBRD(M196%,RD$) 'GET 196 READING
750 V(J)=VAL(RD$) 'CONVERT STRING TO NUMERIC, PUT IN ARRAY
760 IF ABS(V(J))>8 THEN 1040 'CHECK VOLTAGE LIMITS
770 IF J>1 THEN 800
780 RD$=SPACE$(25):CALL IBRD(M485%,RD$) 'GET 485 CURRENT READING
790 I=VAL(RD$) 'CONVERT READING TO NUMERIC
800 CS="F0X":CALL IBWRT(M220%,CS$) 'TURN OFF 220 OUTPUT
810 READ C$ 'READ 705 COMMAND STRING
820 CALL IBWRT(M705%,C$) 'OPEN CROSSPOINTS
830 NEXT J 'LOOP BACK FOR NEXT POINT
840 BEEP
850 CLS
860 CALL IBCMD(BRD0%,D$) 'SEND DEVICE CLEAR
870 PRINT"MEASUREMENTS COMPLETE."
880 PRINT"MEASURED CURRENT";I!
890 FOR J=1 TO 4 'LOOP AND DISPLAY MEASURED VOLTAGES
900 PRINT"V";J;="";V(J)
910 NEXT J
920 PRINT"PRESS ANY KEY TO CALCULATE RESISTIVITIES"
930 A$=INKEY$:IF A$="" THEN 930
940 PA=W*T/(2+I1*D1)*(V(1)-V(3))
950 PB=W*T/(2+I1*D2)*(V(2)-V(4))
960 PAVG=(PA+PB)/2
970 PRINT"PA=";PA
980 PRINT"PB=";PB
990 PRINT"PAVG=";PAVG
1000 PRINT
Program 8
6- and 8-Contact Sample Resistivity (IBM-PC/8573A Version), Continued

1010 INPUT "REPEAT TEST (Y/N)"; A$
1020 IF LEFT$(A$, 1) = "Y" THEN CLS: GOTO 290
1030 GOTO 1150
1040 CLS: BEEP
1050 CALL IBCMD(BRD0%, D$) ' SEND DEVICE CLEAR
1060 PRINT "SAMPLE VOLTAGE IS OVER 8V 7065 LIMIT"
1070 PRINT "DO YOU WISH TO:"
1080 PRINT "(1) RESTART THE MEASUREMENT"
1090 PRINT "(2) ABORT THE PROGRAM"
1100 PRINT
1110 INPUT A
1120 IF A$ = 1 OR A$ = 2 THEN 1070
1130 IF A$ = 2 THEN 1150
1140 CLS: GOTO 290
1150 END 'END PROGRAM
1160 REM ***** 705 COMMAND STRINGS **********
1170 DATA "C03:2004:1X"
1180 DATA "N03:2N04:1X"
1190 DATA "C03:3004:4X"
1200 DATA "N03:3N04:4X"
REFERENCES


Wieder, H.H. Laboratory Notes on Electrical and Galvanomagnetic Measurements

4.1 INTRODUCTION

This section contains the necessary information to service your Model 7065 Hall Effect Card and contains the following information:

4.2 Handling and Cleaning Precautions: Discusses handling precautions and cleaning methods for the Model 7065.

4.3 Performance Verification: Covers the procedures necessary to determine if the card is operating properly.

4.4 Adjustments: Outlines adjustment procedures for the Hall effect card.

4.5 Theory of Operation: Briefly discusses circuit operation from a block diagram viewpoint.

4.6 Special Handling of Static-Sensitive Devices: Reviews some precautions necessary to avoid damaging static-sensitive devices.

4.7 Troubleshooting: Gives some troubleshooting tips for the Model 7065.

4.2 HANDLING AND CLEANING PRECAUTIONS

Because of the high impedance of many circuits on the Model 7065, care should be taken during handling or servicing of the card in order to prevent possible performance degradation because of contamination. The following precautions should be taken when handling the scanner card.

1. Handle the card only at the edges of the board whenever possible. Avoid touching any components not associated with the repair.

2. Do not store or operate the card in an environment where dust could settle on the board. Use dry nitrogen gas to clean dust off the card when necessary.

3. If it is necessary to use solder on the circuit board or around the Teflon insulators, remove the flux from these areas when the repair is complete. Use Freon® TMS or TE or equivalent to remove the flux. Clean cotton swabs or a clean soft brush can be used to help remove the flux. Once all the flux is removed, swab the area with methanol, then blow dry the board with dry nitrogen gas.

4. After cleaning, the card should be placed in a 50°C low-humidity environment for several hours before resuming normal operation.

4.3 PERFORMANCE VERIFICATION

The following paragraphs discuss procedures for verifying offset voltage, input current, and input resistance.

4.3.1 Environmental Conditions

All measurements should be made at an ambient temperature between 18 and 28°C and at a relative humidity less than 70% unless otherwise noted.

4.3.2 Warm Up Period

The Model 7065 should be turned on and allowed to warm up for at least one hour before beginning the verification procedures. Also, the test equipment should be allowed to warm up for the period stated in their respective instruction manuals or specifications.

4.3.3 Recommended Equipment

Table 4-1 summarizes the equipment necessary to perform the verification procedures.
Table 4-1. Performance Verification Equipment

<table>
<thead>
<tr>
<th>Description</th>
<th>Specifications</th>
<th>Manufacturer and Model</th>
</tr>
</thead>
<tbody>
<tr>
<td>Digital Multimeter</td>
<td>100nV DC resolution, ±0.006% accuracy</td>
<td>Keithley 196</td>
</tr>
<tr>
<td>Electrometer/Source Scanner</td>
<td>1fA sensitivity, ±8V DC output</td>
<td>Keithley 617</td>
</tr>
<tr>
<td>4801 Coax Cable</td>
<td></td>
<td>Keithley 705 or 706</td>
</tr>
<tr>
<td>7024 Triax cable</td>
<td></td>
<td>Supplied</td>
</tr>
<tr>
<td>7025 Triax Cable</td>
<td></td>
<td>Supplied</td>
</tr>
<tr>
<td>4802 cable</td>
<td></td>
<td>Supplied</td>
</tr>
<tr>
<td>SC-72 wire</td>
<td></td>
<td>Supplied</td>
</tr>
<tr>
<td>SC-81 BG-7 Cable</td>
<td></td>
<td>Supplied</td>
</tr>
</tbody>
</table>

4.3.4 High-impedance Considerations

Because of the high impedances involved with some of the measurements, the cables should be kept as still as possible during the tests.

4.3.5 Input Current Verification

Connections

Connect the Model 617 Electrometer to the sample input to be verified, as shown in Figure 4-1. Use a Model 7024 triaxial cable to connect the sample input jack to the electrometer. Make sure the ground link is connected between the Model 617 ANALOG OUTPUT COMMON and chassis ground terminals.

Procedure

1. Install the Model 7065 in the scanner mainframe and turn on the power. Allow the unit to warm up for one hour.
2. Using Program 6, select the matrix mode (pole 0).
3. Place the electrometer on the 2nA range, enable zero check, and zero correct the electrometer. Make sure the electrometer guard switch is in the off position.
4. Disable zero check and note the reading on the electrometer display. Verify that the reading is less than 100pA exclusive of noise.
5. Enable zero check on the electrometer and place the unit on the 2pA range.
6. Close crosspoint 5, 4 to enable the high resistivity mode.
7. Disable zero check and verify that the current reading is less than 0.15pA exclusive of noise.
8. Enable zero check and open crosspoint 5, 4 on the scanner.
9. Repeat steps 3 through 8 for the remaining three sample inputs. The electrometer should be connected to the sample input being tested.
Figure 4-1. Connections for Input Current Verification
4.3.6 Input Resistance Verification

Connections

Connect the Keithley Model 617 Electrometer to the Hall card, as shown in Figure 4-2. The electrometer should be connected to the sample input jack being tested through a 7024 triaxial cable. Connect the voltage source low terminal to the Model 7065 analog ground using a supplied SC-72 black wire. Connect the second SC-72 black wire between voltage source output high and electrometer analog output common. Make sure the ground link is removed.

Procedure

1. Install the Hall card in the mainframe and turn on the power.
2. Using Program 6, select the matrix mode (pole 0). Press ENTER.
3. Select the 20nA range on the electrometer, enable zero check, and then zero correct the instrument. Make sure the low-to-ground link on the electrometer is removed, and that the guard switch is in the off position.
4. Make sure the Model 617 voltage source output is turned off, then disable zero check. Wait for the reading to settle.
5. Enable the suppress feature on the electrometer.
6. Enable zero check and program the Model 617 voltage source for a value of +8V, turn on the output.
7. Release zero check and allow the reading to settle. Note and record the current reading as $I_1$.
8. Program the voltage source for an output of $-8V$.
9. Note the current reading on the electrometer, and record the value as $I_2$.
10. Compute the input resistance for the low resistivity setup as follows:

$$ R = \frac{16}{|I_2 - I_1|} $$

11. Verify that the resistance calculated in step 10 is greater than 10GΩ. Note typical values may be as high as 10¹⁵Ω.
12. Enable zero check, select the 2pA range, and disable suppress. Zero correct the electrometer.
13. Turn off the electrometer voltage source output.
14. Close crosspoint 5, 4 to select high resistivity by programming the scanner.
15. Disable zero check, wait for the reading to settle, and then enable suppress.
16. Turn on the electrometer voltage source output. The voltage source should still be programmed to $-8V$.
17. Note and record the current reading as $I_3$.
18. Program the voltage source to $+8V$.
19. Note and record the current reading as $I_4$.
20. Compute the input resistance for the high resistivity setup as follows:

$$ R = \frac{16}{|I_3 - I_4|} $$

21. Verify that the resistance calculated in step 20 is greater than 100TΩ.
22. Turn off the voltage source, enable zero check, and disable suppress.
23. Open crosspoint 5,4 and repeat steps 3 through 22 for the other three sample inputs. The electrometer should be connected to the sample input being tested using the connections shown in Figure 4-2.
Figure 4-2. Connections for Input Resistance Verification
4.3.7 Voltage Offset Verification

Connections

Figure 4-3 shows connection for voltage offset verification. The HI terminal of the DMM is to be connected to terminal 5 of the terminal strip, while the DMM LO terminal and the center contact of the sample input being verified must both be connected to analog ground, as shown on the diagram.

This connection should be made as follows:

1. Prepare the end of a Model 7025 triaxial cable by stripping 1 inch of insulation off the end and cutting off the shields (see Figure 2-6 in Section 2).
2. Twist together the center conductor and one end of an SC-72 wire (black).
3. Place the junction in a small box in between two pieces of foam to prevent air currents from affecting the reading. Also, keep the cable as still as possible.
4. Connect the free end of the black wire to Hall Card analog ground, and connect the triaxial cable to the sample input being tested.

Procedure

1. Install the Model 7065 in the scanner mainframe and turn on the power.
2. Using front panel Program 6, select the matrix mode on the scanner (pole 0).
3. Select the DCV function and the 300mV range on the DMM.
4. Temporarily disconnect the DMM and short its input. Zero the DMM, remove the short, and reconnect it to the Hall card.
5. Close the crosspoint for the sample input being tested, as summarized in Table 4-2. For example, for sample input 1, close 3,1.
6. Verify that the DMM reading is \( \leq 10 \mu V \) exclusive of noise after settling. If necessary, select suitable DMM filtering to quiet the reading.
7. Close crosspoint 5,4 in order to select the high resistivity mode.
8. Allow sufficient time for the reading to settle, then verify that the DMM reading is less than 200\( \mu V \) exclusive of noise. Again, DMM filtering may be required to quiet the reading.

NOTE

If the high resistivity offset reading for one of the channels is above specified limits, the offset can be nulled by using the procedure discussed in paragraph 4.4.

Table 4-2. Crosspoint Closed to Measure Sample Input Voltage Offset

<table>
<thead>
<tr>
<th>Sample Input #</th>
<th>Crosspoint Closed (Column, Row)</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>3,3</td>
</tr>
<tr>
<td>2</td>
<td>3,2</td>
</tr>
<tr>
<td>3</td>
<td>3,3</td>
</tr>
<tr>
<td>4</td>
<td>3,2</td>
</tr>
</tbody>
</table>

9. Press the scanner RESET button to open all crosspoints, then re-zero the DMM.
10. Repeat steps 4 through 9 for the remaining three sample inputs. Be sure to connect the 7025 triax cable to the sample input being test.
Figure 4-3. Connections for Voltage Offset Verification
4.4 ADJUSTMENTS

Offset adjustments for the four sample inputs as well as common mode rejection adjustment are covered in the following paragraphs.

4.4.1 Environmental Conditions

All adjustments should be made at an ambient temperature of 18 to 28°C and at a relative humidity of less than 70%.

4.4.2 Warm Up Period

The Model 7065 should be allowed to warm up for one hour before performing the adjustments. Test equipment should also be allowed to warm up for the period stated in the respective instruction manuals.

4.4.3 Recommended Test Equipment

Table 4-3 summarizes the equipment necessary to make the adjustments.

4.4.4 Adjustment Locations

Figure 4-4 shows the adjustment locations. The four offset adjustments are accessible from the end of the card; each control is adjacent to the associated input connector. The common mode rejection adjustment can be accessed through the hole in the small digital shield.

<table>
<thead>
<tr>
<th>Description</th>
<th>Specifications</th>
<th>Manufacturer and Model</th>
</tr>
</thead>
<tbody>
<tr>
<td>DMM Oscilloscope</td>
<td>100mV sensitivity</td>
<td>Keithley Model 196, TEK 2235</td>
</tr>
<tr>
<td>Scanner</td>
<td>&gt;10MHz bandwidth</td>
<td>Keithley Model 705 or 706</td>
</tr>
<tr>
<td>10kΩ Resistor Extender Card</td>
<td>10mV/div sensitivity</td>
<td>Keithley part # R76-10k</td>
</tr>
<tr>
<td></td>
<td>¼W, 5%</td>
<td>Keithley Model 7061</td>
</tr>
</tbody>
</table>

Table 4-3. Equipment Needed for Adjustments

Figure 4-4. Adjustment Locations
4.4.5 Offset Adjustments

Connections

Connections for making offset adjustments are shown in Figure 4-5. The HI input lead of the DMM is connected to terminal 5 of the strip, while DMM LO is connected to analog ground. The center connector of the sample input jack being adjusted must also be connected to analog ground on the scanner card. See paragraph 4.3.7 for details on connecting the wire and cables together.

Procedure

1. With the power off, install the Model 7065 in the scanner mainframe with all shields in place.
2. Using Program 6, select the matrix mode (pole 0). Press ENTER.
3. Close crosspoint 5,4 to select the high resistivity mode.
4. Select the 300mV DC range on the DMM. Temporarily disconnect the DMM, short its input, and then enable zero. Remove the short and reconnect the DMM to the Hall card.
5. Close the crosspoint for the sample input you are currently adjusting, as summarized in Table 4-4. For example, to measure sample input #1, close 3,1.
6. Adjust the offset control for the selected sample input for a minimum (null) reading on the DMM (less than 20µV is satisfactory). The controls are accessible through small holes in the bracket at the back of the card (see Figure 4-4). Note that it may be necessary to use DMM filtering to minimize noise.
7. Open the crosspoint now closed.
8. Repeat steps 5 through 7 for the remaining three sample inputs. Be sure to connect the sample input being adjusted to analog common, and also make certain the the appropriate crosspoint is closed for the measurement.

Table 4-4. Crosspoints to Close When Adjusting Offsets

<table>
<thead>
<tr>
<th>Sample Input #</th>
<th>Crosspoint* (Column, Row)</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>3,1</td>
</tr>
<tr>
<td>2</td>
<td>3,2</td>
</tr>
<tr>
<td>3</td>
<td>3,3</td>
</tr>
<tr>
<td>4</td>
<td>3,4</td>
</tr>
</tbody>
</table>

*Crosspoint 5,4 must also be closed to select high resistivity.
Figure 4-5. Connections for Voltage Offset Adjustment
4.4.6 Common-Mode Adjustment

Connections

Figure 4-6 shows the necessary connections for this adjustment. The 10kΩ resistor is connected between analog ground on the terminal strip and digital ground, which can be accessed at the screw on the small power supply shield. Connect the oscilloscope high input to analog ground, and connect scope low to digital ground at the shield screw. Use shielded cable between the oscilloscope and the card and resistor (shield, LO center conductor, HI).

Procedure

1. Connect the Model 7065 to the scanner through the Model 7061 Universal Adapter Card, used as an extender to allow access to the adjustment. See the Model 7061 Instruction Guide for more information.
2. Make certain that all Model 7065 shields are in place and properly secured.
3. Set the oscilloscope time base and input attenuator to view the waveform shown in Figure 4-7.
4. Adjust the common mode potentiometer (R23) for best symmetry and minimum amplitude of the waveform.
5. Turn off the power and disconnect the resistor and oscilloscope once the adjustment is complete.

![Diagram](image)

**Figure 4-6. Connections for Common Mode Adjustment**

**Figure 4-7. Common Mode Adjustment Waveform**


4.5 THEORY OF OPERATION

The following paragraphs give a brief description of the major circuits in the Model 7065. Figure 4-8 shows a block diagram of the unit, and the schematic diagram is located on drawing number 7065-I06, found at the end of Section 5.

4.5.1 Relay Switching Circuits

A large part of Model 7065 operation is concerned with connecting different inputs and outputs together. For that reason, a number of relays and solid-state switching elements are found on the card.

K1 through K8 perform input or current switching for the card. Each of these devices is a special low-current relay with a guard shield to minimize the effects of leakage currents. Note that the current input is protected by Q1, Q2, VR1, and VR2 to limit input compliance voltage to \( \pm 12V \).

At the output, switching is performed by solid-state elements U11 through U16. Each of these devices is internally controlled by opto-coupling in order to maintain high analog-to-digital isolation.

Some of the switching elements are directly controlled from scanner mainframe signals passed through J1, while others use information derived from the serial control word. For example K3 and K7 are directly controlled, while K4 and K8 are switched by serial control information.

The serial control information is converted to parallel by U5, a shift register, and then buffered by U7 before being applied to the various relays or switching FETs. R2, C1, CR5, and elements of U6 form a power-up/down fail-safe circuit to prevent relays from being inadvertently closed when power is turned on or off.

4.5.2 Buffers

An important feature of the Model 7065 is the use of on-card buffers which give the card its very high (\( >100\Omega \)) input impedance, allowing the card to be used to measure high-resistivity samples.

U1 through U4 are special high-quality operational amplifiers configured as non-inverting, unity-gain amplifiers. These components provide both buffering as well as a driven guard for the sample inputs. Voltage offset adjustment for these ICs is performed by R14 through R17 in order to maintain optimum input leakage current and guard performance.

Resistivity setup switching is performed by FETs Q3 through Q11. In low resistivity, the buffers are effectively bypassed by turning on appropriate FETs while turning off others. For example, when low impedance is selected, Q3, Q4, Q9, and Q10 will be on, while Q5 through Q8 are off. Conversely, in high resistivity, Q5 through Q8 are on, while Q3, Q4, Q9, and Q10 are turned off.

4.5.3 Power Supply

The power supply section of the Model 7065 has a two-fold purpose: (1) to step up the nominal +6V scanner mainframe supply to \( \pm 12V \), and (2) to provide a high degree of isolation between the digital circuits of the mainframe and the sensitive analog signal paths on the Model 7065. In order to meet these requirements, a trapezoidal-wave chopper step-up circuit with transformer isolation is used.

The nominal 3.4kHz time base of the chopper is generated by U8, a 555 timer, which has its operating frequency set by C8 and R19. The signal is buffered by Q13 before being applied to the chopper made up of Q12, Q14, and associated components. The nominal 6V peak signal is coupled through a 112:4 step-down transformer, T1, and then is coupled again through a 4:380 step-up transformer, T2. This dual-transformer technique is used to ensure maximum analog-to-digital isolation.

At the secondary of T2, the nominal 17V peak AC signal is rectified by CR6 before being filtered and regulated. C4, C7, and U9 provide filtering and regulation for the +12V supply, and C5, C6, and U10 filter and regulate the -12V supply.
Figure 4-8. Model 7065 Block Diagram
4.6 SPECIAL HANDLING OF STATIC-SENSITIVE DEVICES

CMOS and other high-impedance devices are subject to possible static discharge damage because of the high impedance levels involved. When handling such devices (indicated by * in the parts list), use the following precautions:

1. Such devices should be transported and handled only in containers specially designed to prevent or dissipate static build-up. Typically, these devices will be received in anti-static containers of plastic or foam. Keep these parts in their original containers until ready for installation.

2. Remove the devices from their protective containers only at a properly-grounded work station. Also, ground yourself with a suitable wrist strap.

3. Handle the devices only by the body; do not touch the pins.

4. Any printed circuit board into which the device is to be inserted must also be grounded to the bench or table.

5. Use only anti-static type de-soldering tools and grounded-tip soldering irons.

6. Once the device is installed in the PC board, it is usually adequately protected, and normal handling can resume.

4.7 TROUBLESHOOTING

4.7.1 Recommended Equipment

Table 4-5 summarizes the equipment necessary for general troubleshooting.

4.7.2 Troubleshooting Procedure

The troubleshooting procedure is summarized in Table 4-6.

---

<table>
<thead>
<tr>
<th>Description</th>
<th>Manufacturer and Model</th>
<th>Use</th>
</tr>
</thead>
<tbody>
<tr>
<td>DMM</td>
<td>Keithley 196</td>
<td>Measure DC voltages</td>
</tr>
<tr>
<td>Voltage Source</td>
<td>Keithley 230</td>
<td>Apply DC voltages</td>
</tr>
<tr>
<td>Oscilloscope</td>
<td>TEK 2235</td>
<td>View waveforms</td>
</tr>
<tr>
<td>Extender Card</td>
<td>Keithley 7061</td>
<td>Allow circuit access</td>
</tr>
</tbody>
</table>
### Table 4-6. Troubleshooting Summary

<table>
<thead>
<tr>
<th>Step</th>
<th>Item/Component</th>
<th>Required Condition</th>
<th>Comments</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>U8 pins 4 &amp; 8</td>
<td>+6V DC</td>
<td>Reference to digital ground</td>
</tr>
<tr>
<td>2</td>
<td>U8 pin 3</td>
<td>3.4kHz square wave</td>
<td>Chopper time base</td>
</tr>
<tr>
<td>3</td>
<td>T1, pins 6-2</td>
<td>≈6V peak, 3.4kHz</td>
<td>Chopper signal referred to digital ground.</td>
</tr>
<tr>
<td>4</td>
<td>T2, pins 1-3</td>
<td>≈17V peak, 3.4kHz</td>
<td>Chopper signal referred to analog ground.</td>
</tr>
<tr>
<td>5</td>
<td>U9, pin 3</td>
<td>+16V DC, ±20%</td>
<td>Referenced to analog ground.</td>
</tr>
<tr>
<td>6</td>
<td>U10, pin 2</td>
<td>−16V DC, ±20%</td>
<td>+12V supply</td>
</tr>
<tr>
<td>7</td>
<td>TP1</td>
<td>+12V DC, ±5%</td>
<td>−12V supply</td>
</tr>
<tr>
<td>8</td>
<td>TP2</td>
<td>−12V DC, ±5%</td>
<td>Control strobe</td>
</tr>
<tr>
<td>9</td>
<td>U5, pin 1</td>
<td>Pulse train when programming channels</td>
<td>Control data</td>
</tr>
<tr>
<td>10</td>
<td>U5, pin 2</td>
<td>Pulse train when programming channels</td>
<td>Control clock</td>
</tr>
<tr>
<td>11</td>
<td>U5, pin 3</td>
<td>Pulse train when programming channels</td>
<td>Select high resistivity</td>
</tr>
<tr>
<td>12</td>
<td>Scanner programming</td>
<td>Close crosspoint 5,4</td>
<td>Connect source low to analog ground</td>
</tr>
<tr>
<td>13</td>
<td>Current source input (J8)</td>
<td>Connect external +5V source to center conductor</td>
<td>Set DMM to measure DCV.</td>
</tr>
<tr>
<td>14</td>
<td>DMM</td>
<td>Connect DMM between pin 5 of J2, and analog ground</td>
<td>Select buffer #1</td>
</tr>
<tr>
<td>15</td>
<td>Scanner programming</td>
<td>Close crosspoints 2,1 and 3,1</td>
<td>Check out sample 1 buffer</td>
</tr>
<tr>
<td>16</td>
<td>DMM</td>
<td>+5V, ±0.1%</td>
<td>Reversed polarity</td>
</tr>
<tr>
<td>17</td>
<td>Voltage source</td>
<td>Set to −5V</td>
<td>Sample 1 buffer negative input</td>
</tr>
<tr>
<td>18</td>
<td>DMM</td>
<td>−5V, ±0.1%</td>
<td>Select buffer #2</td>
</tr>
<tr>
<td>19</td>
<td>Scanner programming</td>
<td>Open 2,1; 3,1; Close 2,2; 3,2</td>
<td>Sample 2 buffer, negative input</td>
</tr>
<tr>
<td>20</td>
<td>DMM</td>
<td>−5V, ±0.1%</td>
<td>Reversed polarity</td>
</tr>
<tr>
<td>21</td>
<td>Voltage source</td>
<td>+5V</td>
<td>Sample 2 buffer positive input</td>
</tr>
<tr>
<td>22</td>
<td>DMM</td>
<td>+5V, ±0.1%</td>
<td>Select buffer #3</td>
</tr>
<tr>
<td>23</td>
<td>Scanner programming</td>
<td>Open 2,2; 3,2; Close 2,3; 3,3</td>
<td>Sample 3 buffer, positive input</td>
</tr>
<tr>
<td>24</td>
<td>DMM</td>
<td>+5V, ±0.1%</td>
<td>Reversed polarity</td>
</tr>
<tr>
<td>25</td>
<td>Voltage source</td>
<td>−5V</td>
<td>Sample 3 buffer, negative input</td>
</tr>
<tr>
<td>26</td>
<td>DMM</td>
<td>−5V, ±0.1%</td>
<td>Select buffer #4</td>
</tr>
<tr>
<td>27</td>
<td>Scanner programming</td>
<td>Open 2,3; 3,3 Close 2,4; 3,4</td>
<td>Sample 4 buffer, negative input</td>
</tr>
<tr>
<td>28</td>
<td>DMM</td>
<td>−5V, ±0.1%</td>
<td>Reversed polarity</td>
</tr>
<tr>
<td>29</td>
<td>Voltage source</td>
<td>−5V</td>
<td>Sample 4 buffer, positive input</td>
</tr>
<tr>
<td>30</td>
<td>DMM</td>
<td>+5V, ±0.1%</td>
<td>Open all crosspoints</td>
</tr>
<tr>
<td>31</td>
<td>Scanner</td>
<td>Press RESET</td>
<td></td>
</tr>
</tbody>
</table>
SECTION 5
REPLACEABLE PARTS

5.1 INTRODUCTION
This section contains a list of replaceable electrical and mechanical parts for the Model 7065. A component layout drawing and schematic diagram are also included.

5.2 PARTS LISTS
Electrical parts are listed in order of circuit designation in Table 5-1. Table 5-2 summarizes mechanical parts.

5.3 ORDERING INFORMATION
To place a parts order, or to obtain information concerning replacement parts, contact your Keithley representative or the factory (see the inside front cover for addresses). When ordering parts, be sure to include the following information:

1. Hall card model number (7065)
2. Unit serial number
3. Part description
4. Circuit description, if applicable
5. Keithley part number

5.4 FACTORY SERVICE
If the Hall card is to be returned to Keithley Instruments for repair or service, perform the following:

1. Complete the service form at the back of this manual and include it with the card.
2. Carefully pack the card in the original packing carton.
3. Write ATTENTION REPAIR DEPARTMENT on the shipping label.

Note that it is not necessary to return the scanner mainframe with the card.

5.5 COMPONENT LAYOUT AND SCHEMATIC DIAGRAM
A component layout drawing of the Model 7065 is shown in Figure 5-1. Figure 5-2 shows a schematic diagram of the card.
# Table 5-1. Electrical Parts

<table>
<thead>
<tr>
<th>Circuit Designation</th>
<th>Description</th>
<th>Keithley Part Number</th>
</tr>
</thead>
<tbody>
<tr>
<td>AT1</td>
<td>Analog FET, photon coupled, HHF1</td>
<td>IC-524</td>
</tr>
<tr>
<td>C1-C7</td>
<td>Capacitor, 10µF, 25V, Aluminum Electrolytic</td>
<td>C-314-10</td>
</tr>
<tr>
<td>C8</td>
<td>Capacitor, 6800pF, 1%, 100V, mica</td>
<td>C-248-6800p</td>
</tr>
<tr>
<td>C9</td>
<td>Capacitor, 0.02µF, 20%, 500V, Ceramic Disc</td>
<td>C-22-02</td>
</tr>
<tr>
<td>C10</td>
<td>Capacitor, 3.3pF, 100V, ceramic</td>
<td>C-372-3.3p</td>
</tr>
<tr>
<td>C11</td>
<td>Capacitor, 1500pF, 10%, 1000V, ceramic disc</td>
<td>C-64-1500p</td>
</tr>
<tr>
<td>CR1-CR4</td>
<td>Diode, rectifier, 1N395</td>
<td>RF-43</td>
</tr>
<tr>
<td>CR5</td>
<td>Diode, silicon, 1N4148</td>
<td>RF-28</td>
</tr>
<tr>
<td>CR6</td>
<td>Bridge Rectifier, 1.5A, 400PIV, PF40</td>
<td>RF-46</td>
</tr>
<tr>
<td>E1-E17</td>
<td>Terminal, Teflon insulated</td>
<td>TE-105-1</td>
</tr>
<tr>
<td>J2</td>
<td>Connector, terminal strip</td>
<td>CS-518-6</td>
</tr>
<tr>
<td>J3</td>
<td>Connector, insulated BNC</td>
<td>CS-520</td>
</tr>
<tr>
<td>J4-J8</td>
<td>Connector, triax</td>
<td>CS-440</td>
</tr>
<tr>
<td>K1-K8</td>
<td>Relay, reed</td>
<td>RL-70</td>
</tr>
<tr>
<td>Q1, Q2</td>
<td>Transistor, silicon, NPN, GES5818</td>
<td>TG-138</td>
</tr>
<tr>
<td>Q3-Q14*</td>
<td>Transistor, FET, 2N7000</td>
<td>TG-195</td>
</tr>
<tr>
<td>R1</td>
<td>Resistor, 10kΩ, ½W, 10%, Composition</td>
<td>R-1-10k</td>
</tr>
<tr>
<td>R2-R6</td>
<td>Resistor, 10kΩ, ¼W, 5%, Composition</td>
<td>R-76-10k</td>
</tr>
<tr>
<td>R7-R9</td>
<td>Resistor, 330kΩ, ¼W, 5%, Composition</td>
<td>R-76-330k</td>
</tr>
<tr>
<td>R10-R13</td>
<td>Resistor, 1MΩ, ½W, 10%, Composition</td>
<td>R-1-1M</td>
</tr>
<tr>
<td>R14-R17</td>
<td>Potentiometer, 100kΩ, 0.75W</td>
<td>RP-89-100k</td>
</tr>
<tr>
<td>R18</td>
<td>Resistor, thick film</td>
<td>TR-177-1</td>
</tr>
<tr>
<td>R19</td>
<td>Resistor, 30.1kΩ, ½W, 1%</td>
<td>R-88-30.1k</td>
</tr>
<tr>
<td>R20</td>
<td>Resistor, 180kΩ, ½W, 5%, Composition</td>
<td>R-76-180k</td>
</tr>
<tr>
<td>R21; R22</td>
<td>Resistor, 22kΩ, ⅛W, 5%, Composition</td>
<td>R-76-22k</td>
</tr>
<tr>
<td>R23</td>
<td>Potentiometer, 10kΩ, 0.5W</td>
<td>RP-97-10k</td>
</tr>
<tr>
<td>R24-R27</td>
<td>Resistor, 22MΩ, ¼W, 10%, Composition</td>
<td>R-76-22M</td>
</tr>
<tr>
<td>R28</td>
<td>Resistor, 330Ω, ¼W, 5%, Composition</td>
<td>R-76-330</td>
</tr>
<tr>
<td>R29-R32</td>
<td>Resistor, 1kΩ, ¼W, 5%, Composition</td>
<td>R-76-1k</td>
</tr>
<tr>
<td>R33</td>
<td>Resistor, 4.7Ω, 1W, 10%, Wirewound, Fusible</td>
<td>R-334-4.7</td>
</tr>
<tr>
<td>R34-R42</td>
<td>Resistor, 1MΩ, ¼W, 5%, Composition</td>
<td>R-76-1M</td>
</tr>
<tr>
<td>U1-U4*</td>
<td>IC, operational amplifier, OPA104</td>
<td>IC-519</td>
</tr>
<tr>
<td>U5*</td>
<td>IC, CMOS shift register, 4094</td>
<td>IC-251</td>
</tr>
<tr>
<td>U6*</td>
<td>IC, CMOS quad NOR gate, 4011</td>
<td>IC-102</td>
</tr>
<tr>
<td>U7*</td>
<td>IC, Bus buffer, 74HC241</td>
<td>IC-520</td>
</tr>
<tr>
<td>U8*</td>
<td>IC, CMOS 555 timer</td>
<td>IC-521</td>
</tr>
<tr>
<td>U9</td>
<td>IC, +12V regulator, 78L12</td>
<td>IC-522</td>
</tr>
<tr>
<td>U10</td>
<td>IC, -12V regulator, 79L12</td>
<td>IC-523</td>
</tr>
<tr>
<td>U11-U16</td>
<td>IC, Photovoltaic relay, 3301</td>
<td>IC-525</td>
</tr>
<tr>
<td>T1</td>
<td>Transformer</td>
<td>TR-247</td>
</tr>
<tr>
<td>T2</td>
<td>Transformer</td>
<td>TR-248</td>
</tr>
<tr>
<td>TP1, TP2</td>
<td>Pin, contact</td>
<td>24249</td>
</tr>
<tr>
<td>VR1-VR2</td>
<td>Zener diode, 12V, 100mA, 1N5349B</td>
<td>DZ-72-1</td>
</tr>
<tr>
<td>W1, W2</td>
<td>Connector, jumper</td>
<td>CS-476</td>
</tr>
</tbody>
</table>

*These parts are static sensitive. See paragraph 4.6 for handling precautions.
### Table 5-2. Mechanical Parts

<table>
<thead>
<tr>
<th>Quantity</th>
<th>Description</th>
<th>Keithley Part Number</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Lug for J8</td>
<td>LU-100</td>
</tr>
<tr>
<td>2</td>
<td>Washer for J8</td>
<td>WN-13</td>
</tr>
<tr>
<td>6</td>
<td>Cap for BNC and triax jacks</td>
<td>CAP-18</td>
</tr>
<tr>
<td>1</td>
<td>Bracket, BNC</td>
<td>7065-302</td>
</tr>
<tr>
<td>1</td>
<td>Top Analog Shield</td>
<td>7065-304</td>
</tr>
<tr>
<td>1</td>
<td>Digital Shield</td>
<td>7065-307</td>
</tr>
<tr>
<td>1</td>
<td>Bottom Assembly Shield</td>
<td>7065-121</td>
</tr>
<tr>
<td>5</td>
<td>#4-40 × 1 Phillips pan head modified screw for shields</td>
<td>7065-308</td>
</tr>
<tr>
<td>3</td>
<td>#4-40 × 3/16 Phillips pan head screws for attaching BNC</td>
<td></td>
</tr>
<tr>
<td></td>
<td>bracket to PC board.</td>
<td></td>
</tr>
</tbody>
</table>
Figure 5-1. Model 7065, Component Location Drawing
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KEITHLEY

Service Form

Model No. _____________________ Serial No. _____________________ Date __________
Name and Telephone No. ______________________________________________________
Company _____________________________________________________________

List all control settings, describe problem and check boxes that apply to problem. ________________________________

☐ Intermittent ☐ Analog output follows display ☐ Particular range or function bad; specify
☐ IEEE failure ☐ Obvious problem on power-up ☐ Batteries and fuses are OK
☐ Front panel operational ☐ All ranges or functions are bad ☐ Checked all cables

Display or output (check one)
☐ Drifts ☐ Unable to zero
☐ Unstable ☐ Will not read applied input
☐ Overload

☐ Calibration only ☐ Certificate of calibration required
☐ Data required
(attach any additional sheets as necessary)

Show a block diagram of your measurement system including all instruments connected (whether power is turned on or not). Also, describe signal source.

Where is the measurement being performed? (factory, controlled laboratory, out-of-doors, etc.) ________________________________

What power line voltage is used? _____________________ Ambient temperature? _____________ °F
Relative humidity? _____________________ Other? _____________________

Any additional information. (If special modifications have been made by the user, please describe.) ________________________________

Be sure to include your name and phone number on this service form.